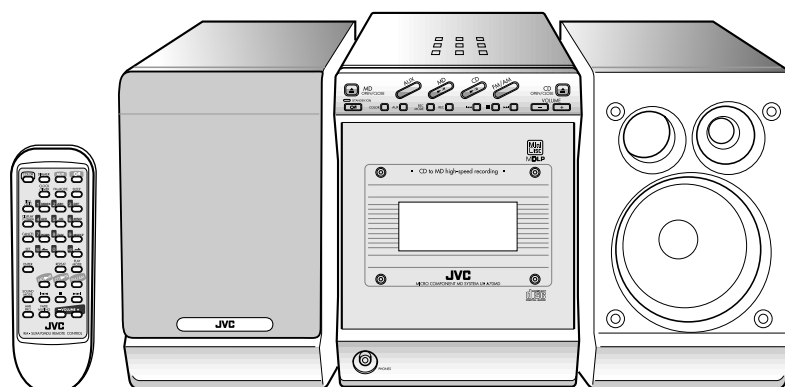


JVC

SERVICE MANUAL

MICRO COMPONENT MD SYSTEM

UX-A70MD



MDLP



COMPACT
disc
DIGITAL AUDIO

Area Suffix

UB	Hong Kong
US	Singapore
UT	Taiwan
UF	China
UJ	U.S.Military

Contents

Safety precautions	1-2	Maintenance of laser pickup	1-34
Preventing static electricity	1-3	Replacement of laser pickup	1-34
Important for laser products	1-5	Maintenance of MD pickup	1-35
Disassembly method	1-6	Procedures of changing	
Main adjustment	1-28	the MD pickup	1-35
Flow of functional operation		Description of major ICs	1-36~56
until TOC read (CD)	1-32		
Flow of functional operation			
until TOC read (MD)	1-33		

Safety Precautions

1. This design of this product contains special hardware and many circuits and components specially for safety purposes. For continued protection, no changes should be made to the original design unless authorized in writing by the manufacturer. Replacement parts must be identical to those used in the original circuits. Services should be performed by qualified personnel only.
2. Alterations of the design or circuitry of the product should not be made. Any design alterations of the product should not be made. Any design alterations or additions will void the manufacturer's warranty and will further relieve the manufacture of responsibility for personal injury or property damage resulting therefrom.
3. Many electrical and mechanical parts in the products have special safety-related characteristics. These characteristics are often not evident from visual inspection nor can the protection afforded by them necessarily be obtained by using replacement components rated for higher voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in the Parts List of Service Manual. Electrical components having such features are identified by shading on the schematics and by (\triangle) on the Parts List in the Service Manual. The use of a substitute replacement which does not have the same safety characteristics as the recommended replacement parts shown in the Parts List of Service Manual may create shock, fire, or other hazards.
4. The leads in the products are routed and dressed with ties, clamps, tubings, barriers and the like to be separated from live parts, high temperature parts, moving parts and/or sharp edges for the prevention of electric shock and fire hazard. When service is required, the original lead routing and dress should be observed, and it should be confirmed that they have been returned to normal, after re-assembling.

5. Leakage current check (Electrical shock hazard testing)

After re-assembling the product, always perform an isolation check on the exposed metal parts of the product (antenna terminals, knobs, metal cabinet, screw heads, headphone jack, control shafts, etc.) to be sure the product is safe to operate without danger of electrical shock.

Do not use a line isolation transformer during this check.

- Plug the AC line cord directly into the AC outlet. Using a "Leakage Current Tester", measure the leakage current from each exposed metal parts of the cabinet, particularly any exposed metal part having a return path to the chassis, to a known good earth ground. Any leakage current must not exceed 0.5mA AC (r.m.s.)

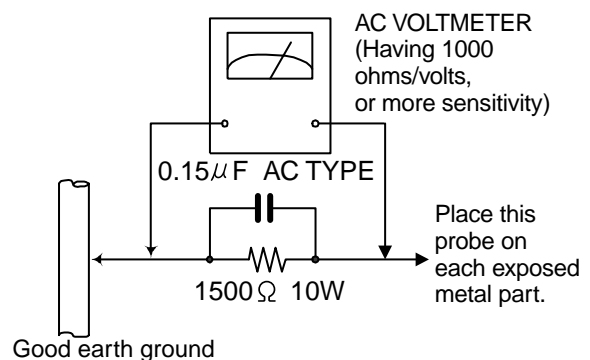
- Alternate check method

Plug the AC line cord directly into the AC outlet. Use an AC voltmeter having, 1,000 ohms per volt or more sensitivity in the following manner. Connect a 1,500 Ω 10W resistor paralleled by a 0.15 μ F AC-type capacitor between an exposed metal part and a known good earth ground.

Measure the AC voltage across the resistor with the AC voltmeter.

Move the resistor connection to each exposed metal part, particularly any exposed metal part having a return path to the chassis, and measure the AC voltage across the resistor. Now, reverse the plug in the AC outlet and repeat each measurement. voltage measured Any must not exceed 0.75 V AC (r.m.s.).

This corresponds to 0.5 mA AC (r.m.s.).



Warning

1. This equipment has been designed and manufactured to meet international safety standards.
2. It is the legal responsibility of the repairer to ensure that these safety standards are maintained.
3. Repairs must be made in accordance with the relevant safety standards.
4. It is essential that safety critical components are replaced by approved parts.
5. If mains voltage selector is provided, check setting for local voltage.

CAUTION Burrs formed during molding may be left over on some parts of the chassis. Therefore, pay attention to such burrs in the case of performing repair of this system.

Preventing static electricity

Electrostatic discharge (ESD), which occurs when static electricity stored in the body, fabric, etc. is discharged, can destroy the laser diode in the traverse unit (optical pickup). Take care to prevent this when performing repairs.

1.1. Grounding to prevent damage by static electricity

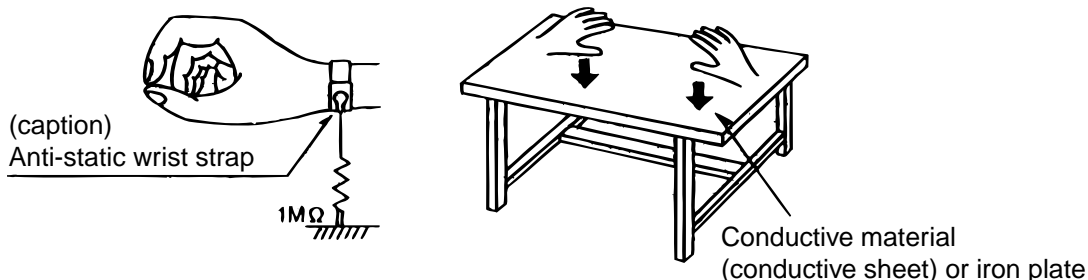
Static electricity in the work area can destroy the optical pickup (laser diode) in devices such as DVD players. Be careful to use proper grounding in the area where repairs are being performed.

1.1.1. Ground the workbench

1. Ground the workbench by laying conductive material (such as a conductive sheet) or an iron plate over it before placing the traverse unit (optical pickup) on it.

1.1.2. Ground yourself

1. Use an anti-static wrist strap to release any static electricity built up in your body.

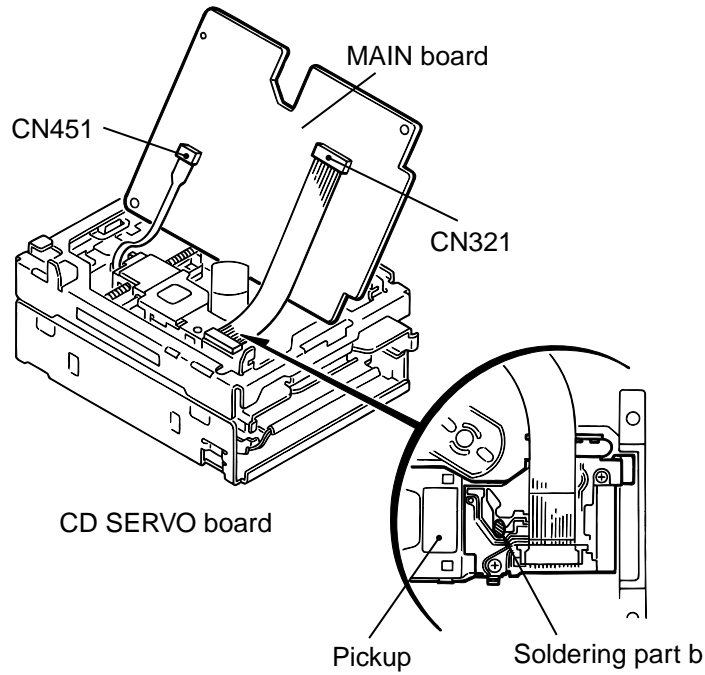


1.1.3. Handling the optical pickup

1. In order to maintain quality during transport and before installation, both sides of the laser diode on the replacement optical pickup are shorted. After replacement, return the shorted parts to their original condition. (Refer to the text.)
2. Do not use a tester to check the condition of the laser diode in the optical pickup. The tester's internal power source can easily destroy the laser diode.

1.2. Handling the traverse unit (optical pickup)

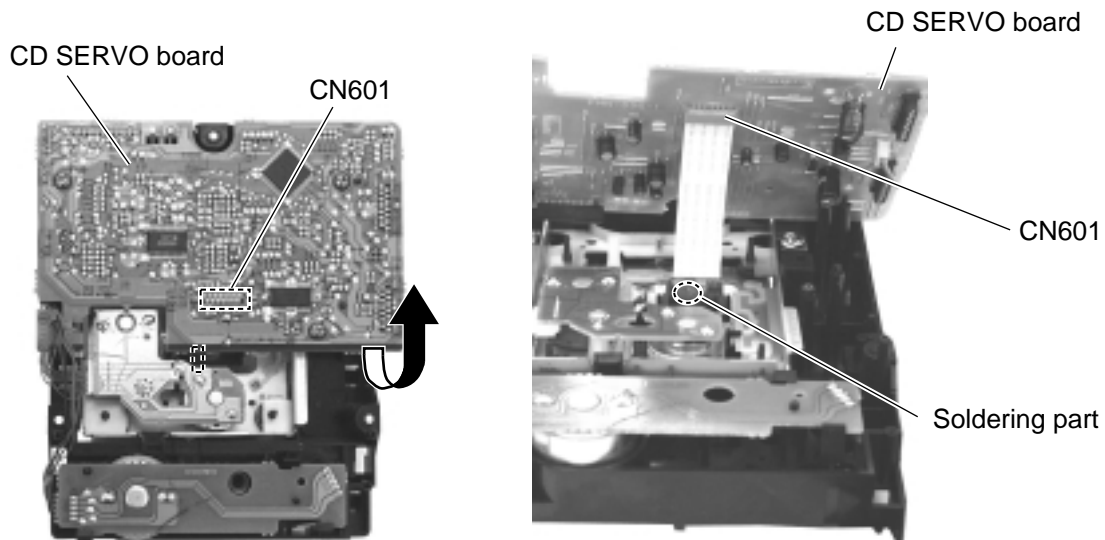
1. Do not subject the traverse unit (optical pickup) to strong shocks, as it is a sensitive, complex unit.
2. Cut off the shorted part of the flexible cable using nippers, etc. after replacing the optical pickup. For specific details, refer to the replacement procedure in the text. Remove the anti-static pin when replacing the traverse unit. Be careful not to take too long a time when attaching it to the connector.
3. Handle the flexible cable carefully as it may break when subjected to strong force.
4. It is not possible to adjust the semi-fixed resistor that adjusts the laser power. Do not turn it



1.3. Cautions on removing the CD traverse unit

* For removing the CD traverse unit in detail, refer to the "Adjustment Method" section of this manual.

1. Before disconnecting the flexible wire from the connector CN601 on the CD SERVO board, solder the part shown in the figure below.
 (Note: If the flexible wire is disconnected from the CN601 without presoldering, it may cause breakdown of the CD pickup assembly.)
2. When reassembling the CD traverse unit, be sure to remove the solder from the soldered part after reconnecting the flexible wire to the CN601.



Important for laser products

1. CLASS 1 LASER PRODUCT

2. DANGER : Invisible laser radiation when open and interlock failed or defeated. Avoid direct exposure to beam.

3. CAUTION : There are no serviceable parts inside the Laser Unit. Do not disassemble the Laser Unit. Replace the complete Laser Unit if it malfunctions.

4. CAUTION : The compact disc player uses invisible laserradiation and is equipped with safety switches which prevent emission of radiation when the drawer is open and the safety interlocks have failed or are defeated. It is dangerous to defeat the safety switches.

5. CAUTION : If safety switches malfunction, the laser is able to function.

6. CAUTION : Use of controls, adjustments or performance of procedures other than those specified herein may result in hazardous radiation exposure.

⚠ CAUTION Please use enough caution not to see the beam directly or touch it in case of an adjustment or operation check.

WARNING : Osynlig laserstråling är denna del är öppnad och spårren är urkopplad. Betrakta ej strålen.

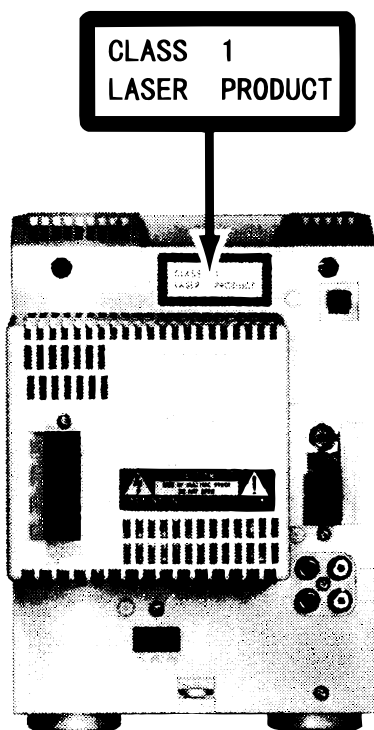
VARO : Avattaessa ja suojalukitus ohitettaessa olet alttiina näkymättömälle lasersäteilylle. Älä katso säteeseen.

ADVARSEL : Usynlig laserstråling ved åbning , når sikkerhedsafbrydere er ude af funktion. Undgå udsættelse for stråling.

ADVARSEL : Usynlig laserstråling ved åpning, når sikkerhetsbryteren er avslott. unngå utsettelse for stråling.

REPRODUCTION AND POSITION OF LABELS

WARNING LABEL



Disassembly method

<Main body>

■ Removing the rear cover

(See Fig.1 and 2)

1. Remove the nine screws **A** attaching the rear cover on the back of the body.
2. Remove the two screws **B** attaching the rear cover on the bottom of the body.
3. Unlock the speaker terminal and the antenna terminal, then remove the rear cover backward with releasing the hooks.

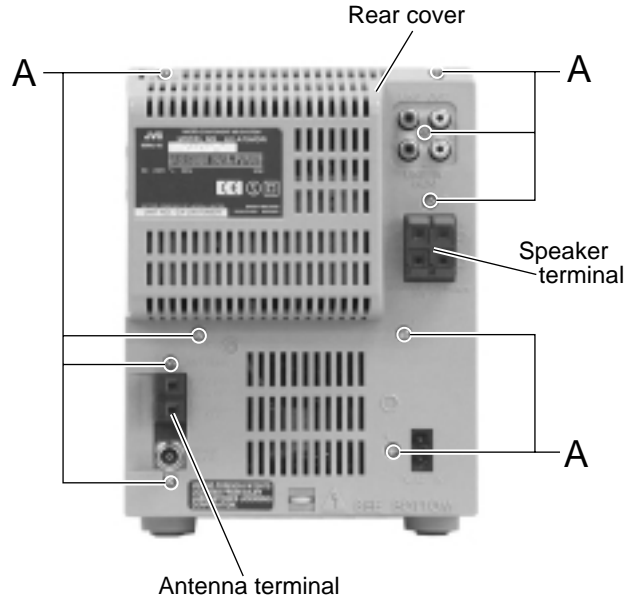


Fig.1

■ Removing the side panels

(See Fig.3 and 4)

- Prior to performing the following procedure, remove the rear cover.
1. Remove each side panel backward while releasing the joints **a**.

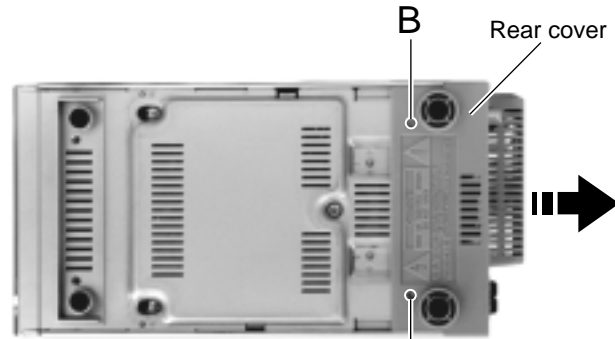


Fig.2

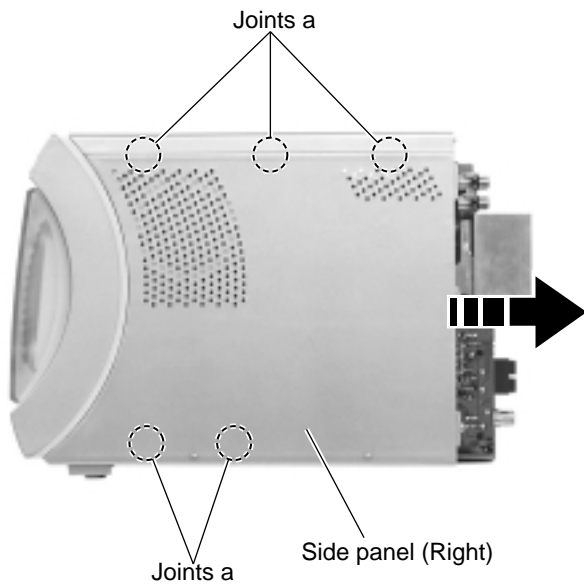


Fig.4

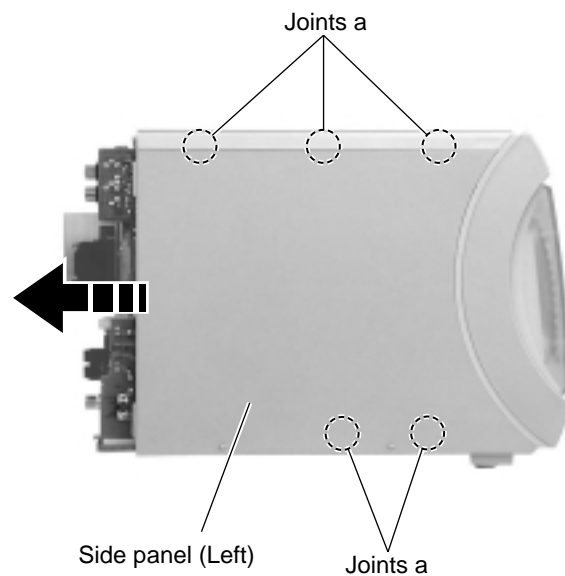
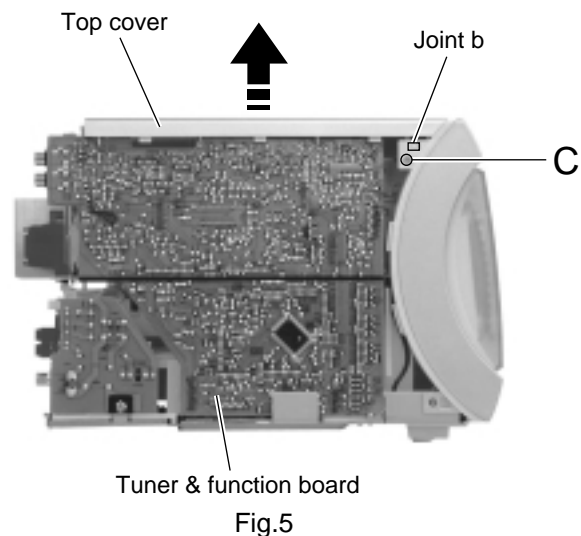


Fig.3

■ Removing the top cover (See Fig.5 and 6)

- Prior to performing the following procedure, remove the rear cover and the side panels.

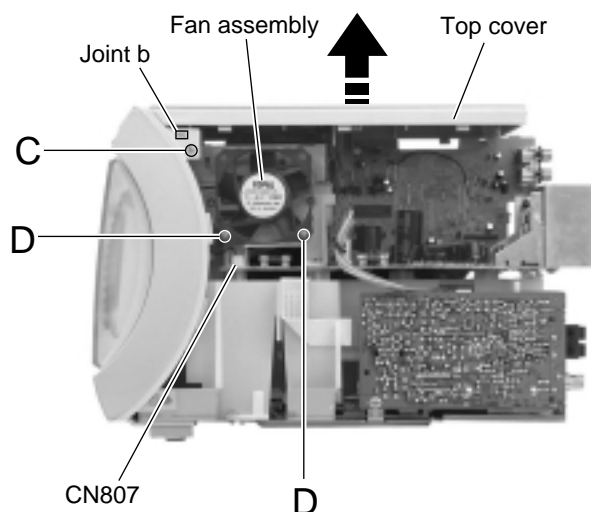
1. Remove the two screws **C** on each side of the body.
2. Release the joint **b** on each side of the body and remove the top cover backward and upward.



■ Removing the fan assembly (See Fig.6)

- *Prior to performing the following procedure, remove the rear cover, the right and left side panels.

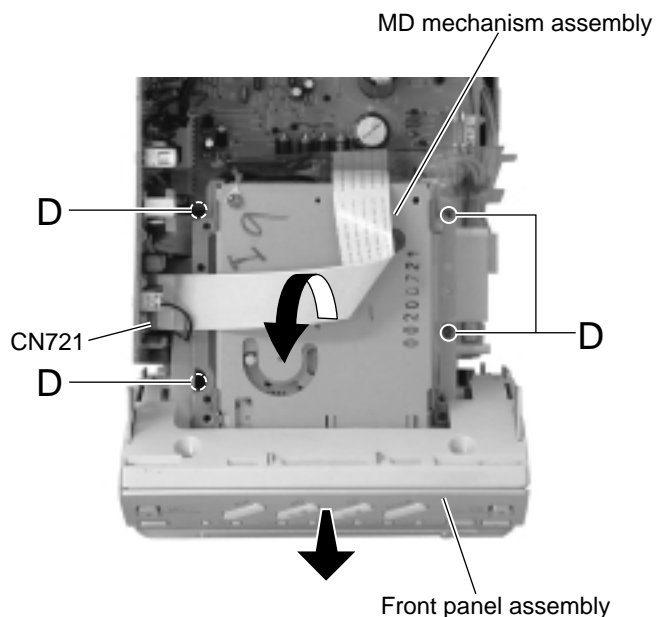
1. Remove the two screws **D** on the right side of the body and pull out the fan assembly sideward.
2. Disconnect the wire from connector CN807 on the main board.



■ Removing the MD mechanism assembly unit (See Fig.7)

- Prior to performing the following procedure, remove the rear cover, the side panels, the top cover and the fan assembly.

1. Disconnect the card wire from connector CN721 on the tuner & function board.
2. Remove the four screws **D** and the MD mechanism assembly backward and upward while pulling the front panel assembly forward.



**■ Removing the tuner & function board
(See Fig.8)**

• Prior to performing the following procedure, remove the rear cover, the side panels and the top cover.

1. Disconnect the card wire from connector CN701, CN705, CN707, CN708 and CN721 on the tuner & function board on the right side of the body. Similarly, disconnect the harness from CN706.

CAUTION: For the card wire connected to CN707, disconnect it after performing the following procedure 2 and 3.

2. Disconnect connector CN711 and CN712 on the tuner & function board from CN801 and CN802 on the main board by pulling them outward.
3. Disconnect connector CN703 and CN704 on the tuner & function board from CN603 and CN604 on the CD servo board by pulling them upward.

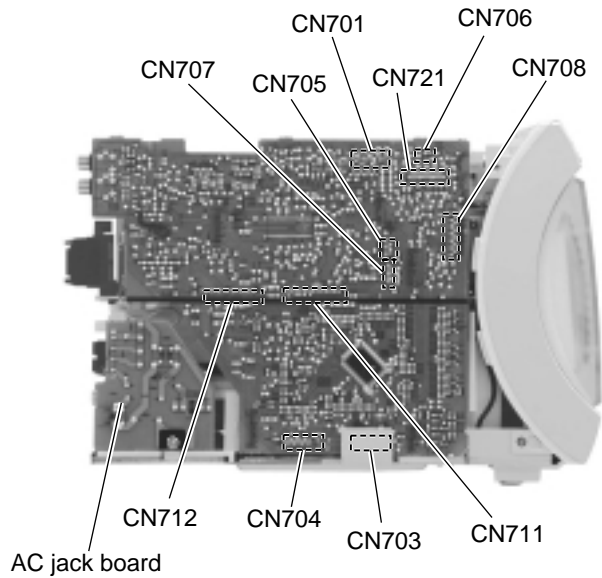


Fig.8

■ Removing the AC jack board (See Fig.9)

• Prior to performing the following procedure, remove the rear cover and the right side panel.

1. Remove the screw **E** on the right side of the body and release the AC jack board from the two joints **c**.
2. Disconnect the harness from connector CN805 on the AC jack board.

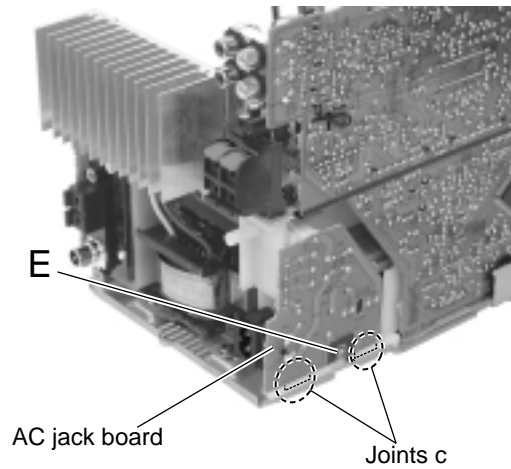


Fig.9

**■ Removing the antenna terminal board
(See Fig.10)**

• Prior to performing the following procedure, remove the rear cover and the left side panel.

1. Disconnect the card wire from connector CN1 on the left side of the body.
2. Remove the screw **F** and release the antenna terminal board from the joint **d** upward.

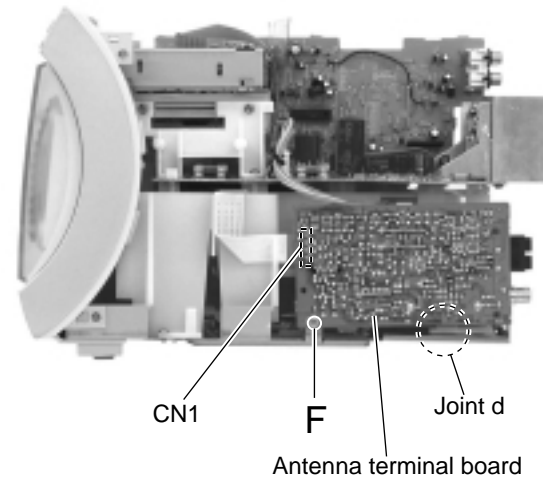


Fig.10

**■ Removing the front panel assembly unit
(See Fig.11 to 15)**

• Prior to performing the following procedure, remove the rear cover, the side panels, the top cover, the MD mechanism assembly and the tuner & function board.

1. Disconnect the wire from connector CN804 and CN806 on the main board. Disconnect the wire from W802 pin terminal.
2. Remove the two screws **G** on the bottom of the body.
3. Remove the lower screw **H** on each side of the body.
4. Release the lower joint **e** on each side of the body. Pull out the front panel assembly toward the front.

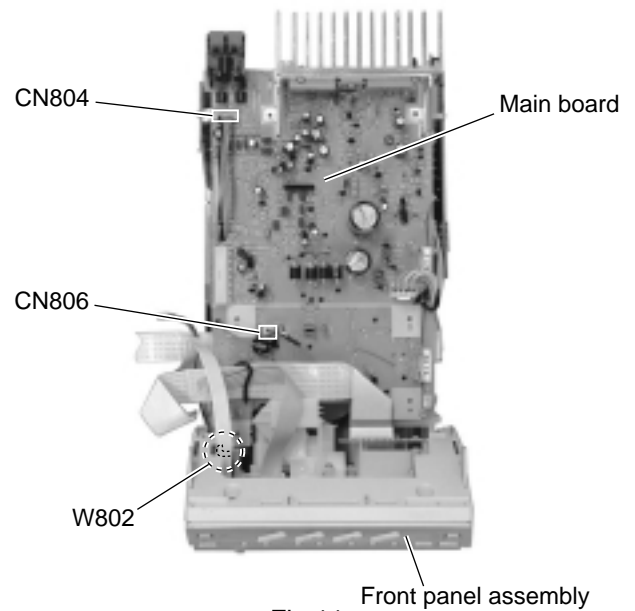


Fig.11

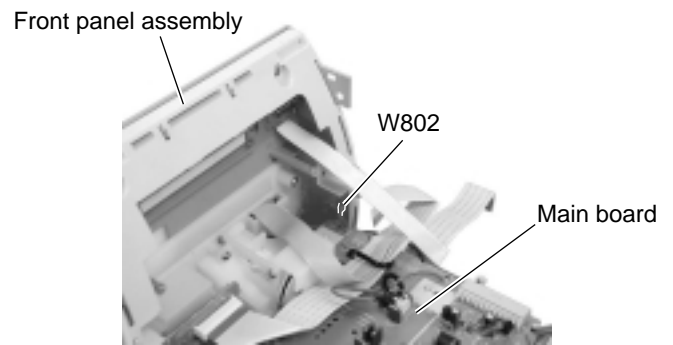


Fig.12

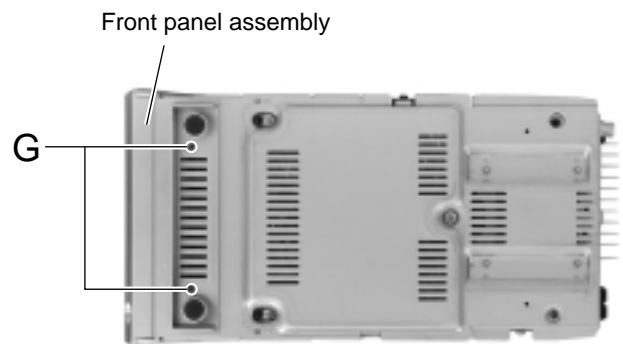


Fig.13

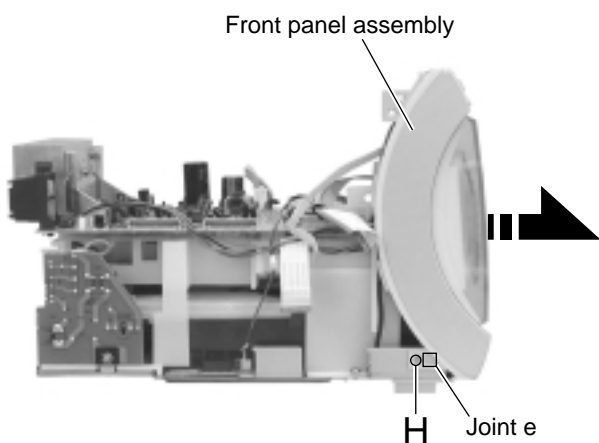


Fig.15

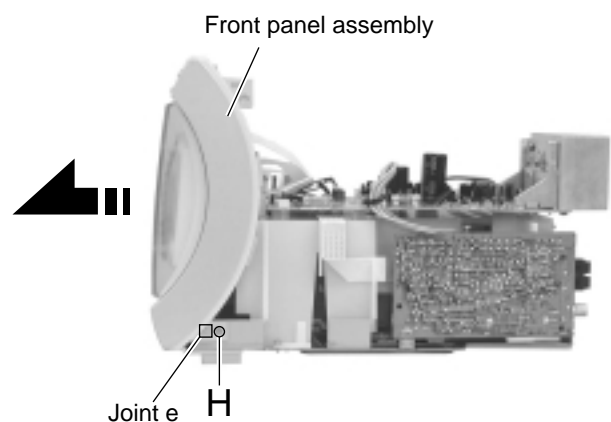


Fig.14

■ **Removing the main board / heat sink**
(See Fig.16 to 18)

• Prior to performing the following procedure, remove the rear cover, the side panels, the top cover, the MD mechanism assembly and the tuner & function board.

1. Disconnect the wire from connector CN803, CN804 and CN806 on the main board respectively. Disconnect the wire from W802 pin terminal.
2. Remove the screw **I** attaching the main board and the screw **J** attaching the wire terminal.

CAUTION: When reassembling, fit the slots of the main board to the two bosses.

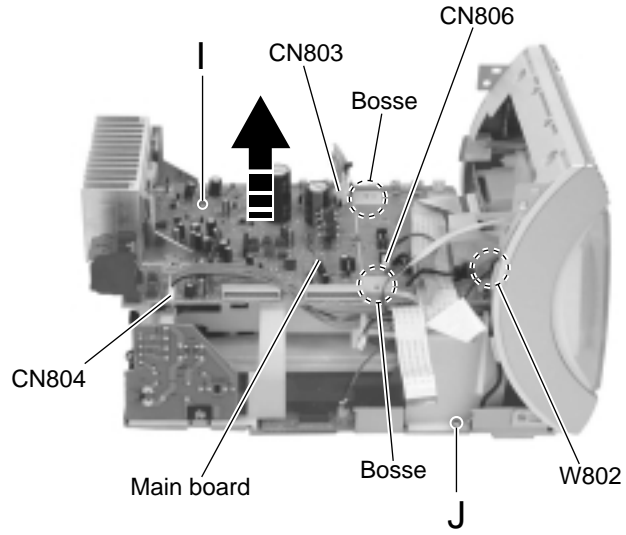


Fig.16

3. Remove the three screws **K** on the heat sink bracket.
4. Remove the two screws **L** on the back of the main board.

CAUTION: When removing the heat sink bracket, disengage the two joints **d** on the back of the main board.

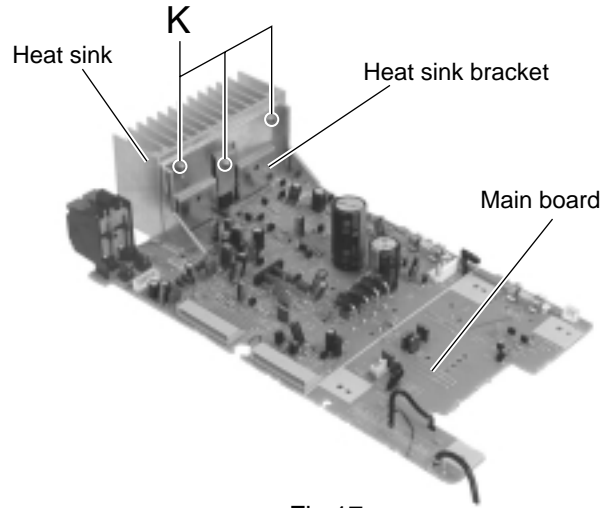


Fig.17

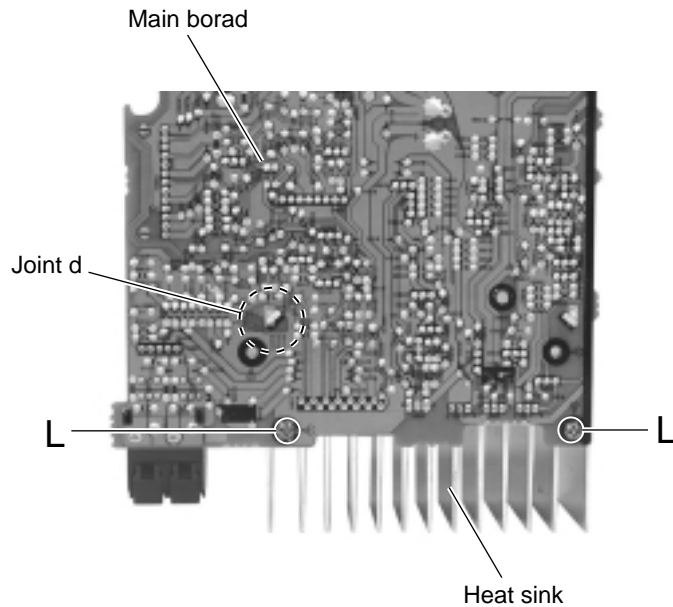


Fig.18

■ Removing the CD mechanism assembly and the CD servo board

(See Fig.19 to 24)

• Prior to performing the following procedure, remove the rear cover, the side panels, the top cover, the MD mechanism assembly, the tuner & function board, the AC jack board, the antenna terminal board, the front panel assembly and the main board.

1. Cut off the two tie band on the back of the CD mechanism assembly top cover.
2. Remove the screw **M** and the top cover.
3. Move the lever **f** in the direction of the arrow to draw the loading tray manually.
4. Remove the screw **N** on the CD tray and pull out the CD tray from the CD mechanism assembly.
5. Remove the two screws **O** attaching the clamper base on top of the CD mechanism assembly. Release the two joints **g** with the CD mechanism assembly and remove the clamper base upward.
6. Remove the three screws **P** attaching the CD mechanism assembly.
7. Remove the three screws **Q** attaching the CD servo board on the back of the CD mechanism assembly.
8. Disconnect the harness from connector P031 on the CD mechanism assembly. Disconnect the harness and the card wire from connector CN602 and CN601 on the CD servo control board respectively.

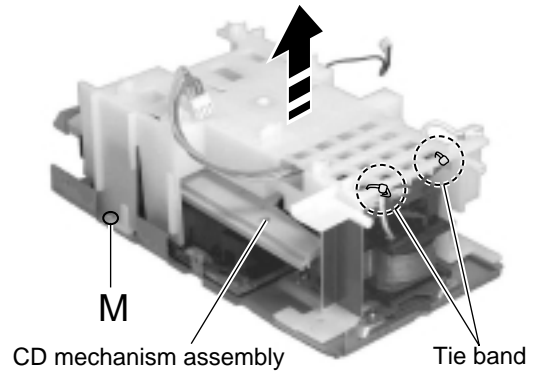


Fig.19

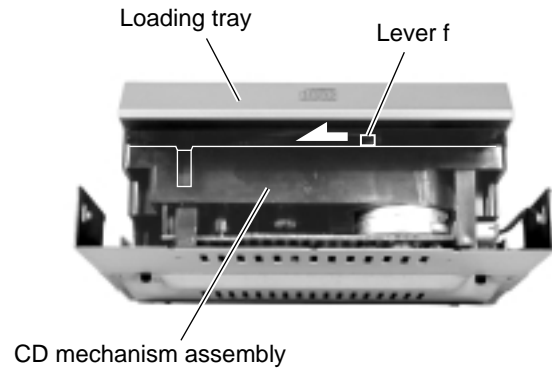


Fig.20

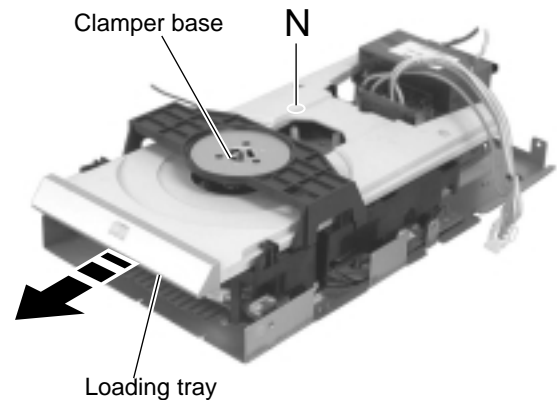


Fig.21

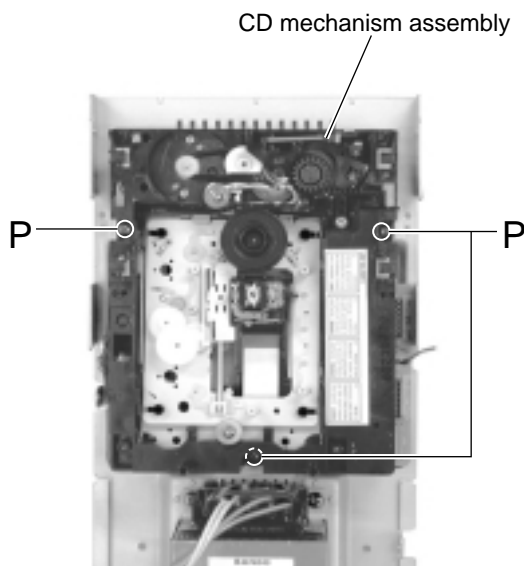


Fig.23

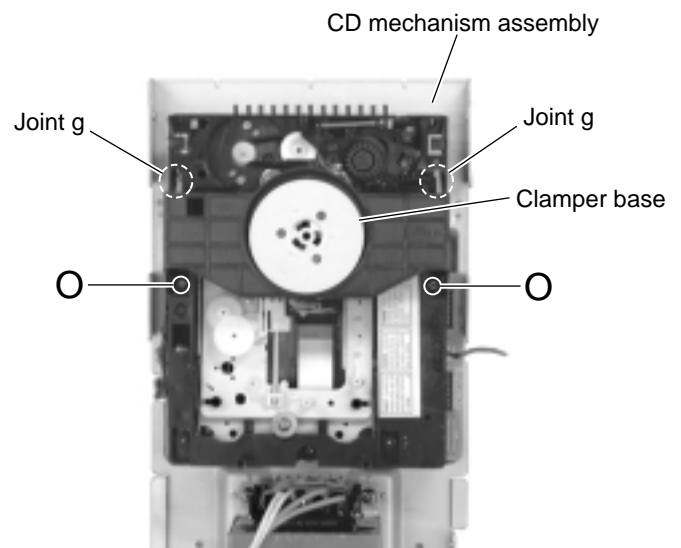


Fig.22

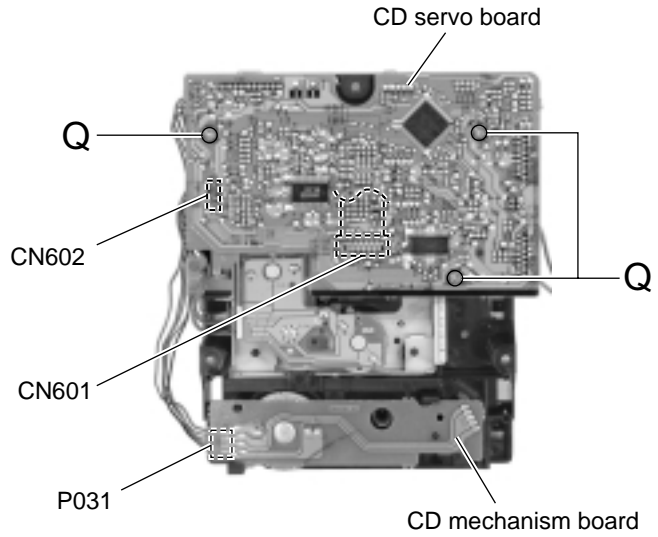
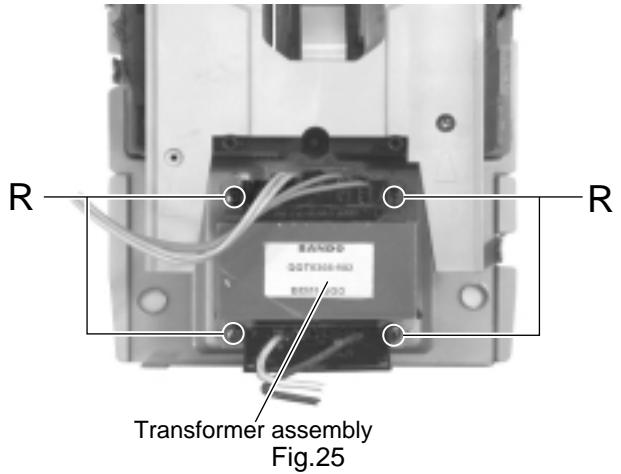


Fig.24

**■ Removing the transformer assembly
(See Fig.19 and 25)**

Prior to performing the following procedure, remove the rear cover, the side panels, the top cover, the MD mechanism assembly, the tuner & function board, the AC jack board, the antenna terminal board, the front panel assembly and the main board.

1. Cut off the two wire clamp on the back of the CD mechanism assembly upper cover.
2. Remove the screw **M** and the CD mechanism assembly upper cover.
3. Remove the four screws **R** attaching the transformer assembly.



Transformer assembly
Fig.25

<MD mechanism assembly unit>

- Prior to performing the following procedure, remove the rear cover, the side panels, the top cover unit and the MD mechanism assembly unit.

**■ Removing the MD mechanism assembly
(See Fig.26 to 28)**

1. Remove the four screws **S** attaching the bracket (a) and (b) on both sides of the MD mechanism assembly unit.
2. Disconnect the card wire from connector CN521 on the MD mechanism board. Remove the MD mechanism assembly bottom cover downward.

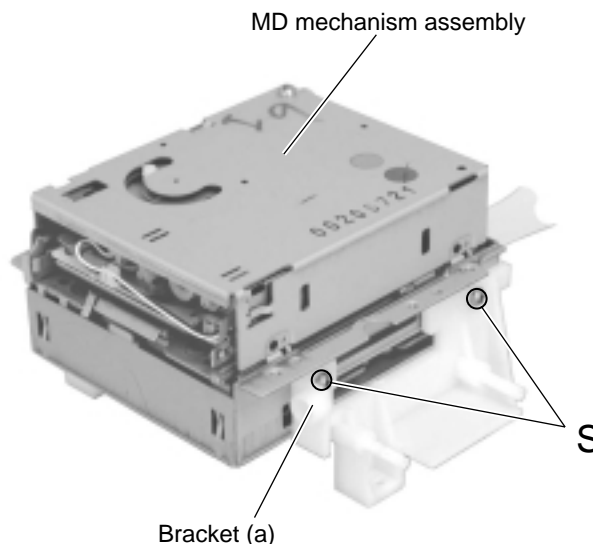


Fig.26

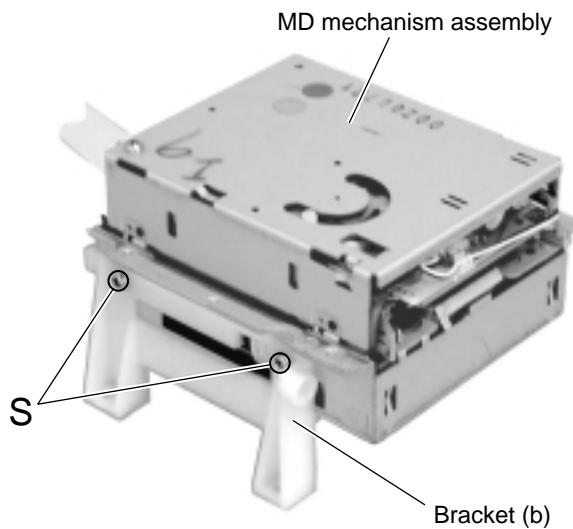


Fig.27

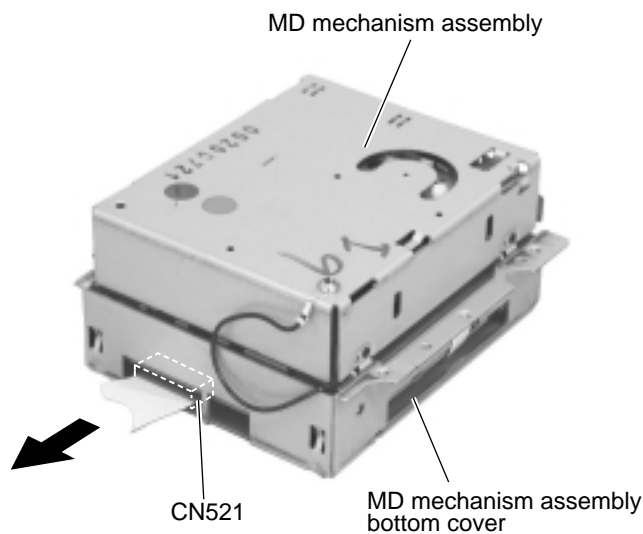


Fig.28

<Front panel assembly unit>

- Prior to performing the following procedure, remove the rear cover, the side panels, the top cover, the MD mechanism assembly unit, the tuner & function board and the front panel assembly unit.

■ Removing the relay board (See Fig.29)

1. Disconnect the card wire from connector CN908 on the relay board respectively.
2. Remove the screw **T** attaching the relay board.

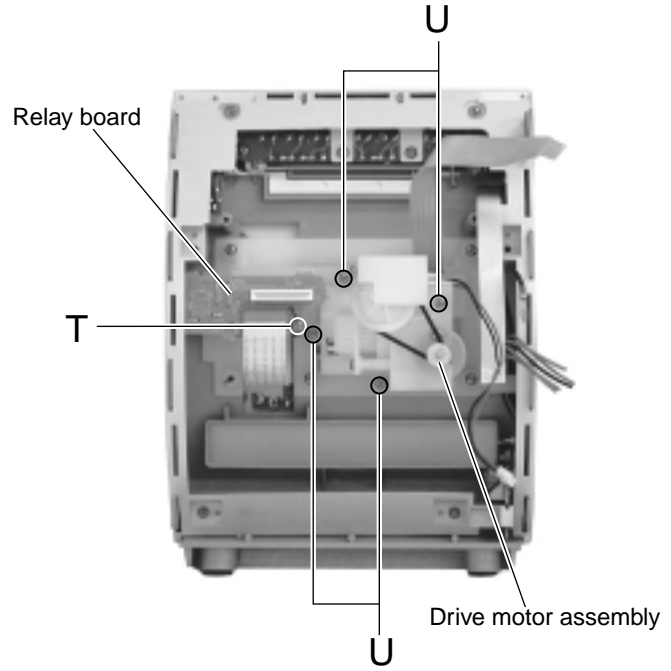


Fig.29

■ Removing the drive motor assembly (See Fig.29 and 30)

1. Remove the four screws **U** attaching the drive motor assembly.
2. Remove the belt and the screw **V** attaching the drive motor.

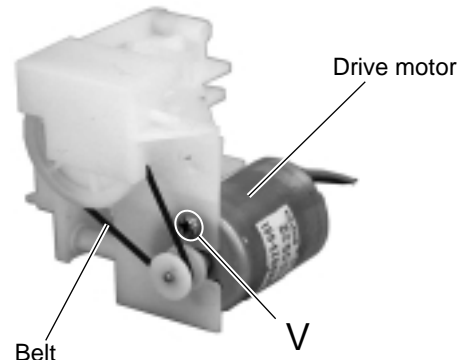


Fig.30

■ Removing the headphone jack board (See Fig.31)

1. Remove the six screws **W** attaching the bracket (c), and detach the headphone jack board.

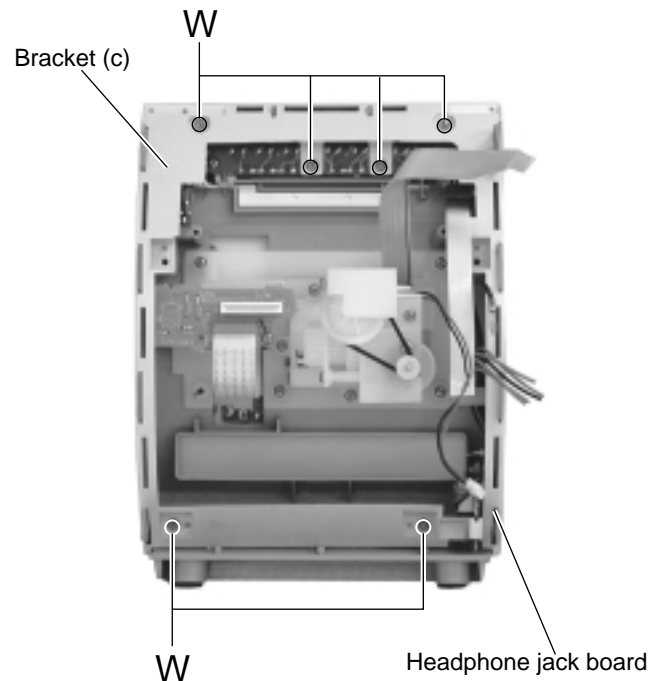


Fig.31

■ Removing the key switch board / (d) board (See Fig.32 and 33)

· Prior to performing the following procedure, remove the bracket (c).

1. Remove the three screws **X** attaching the key switch board.
2. Remove the spring **g** and the screw **Y** attaching the (d) board.

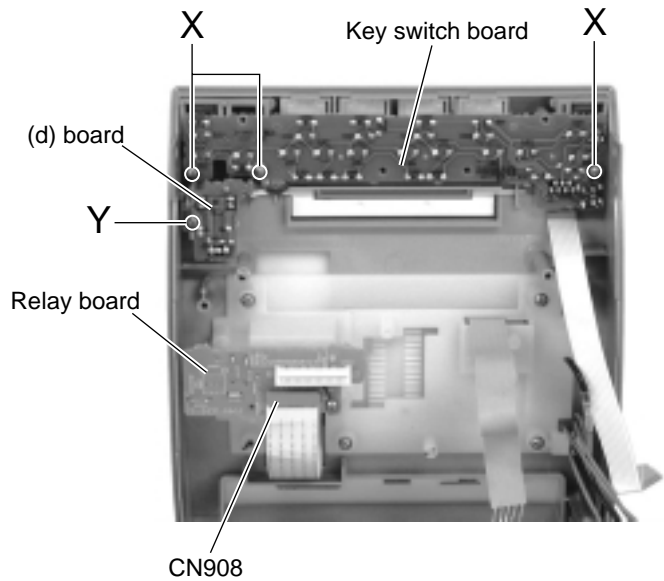


Fig.32

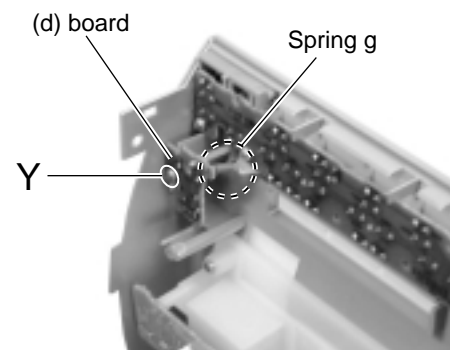


Fig.33

**■ Removing the LCD board / sub board
(See Fig.32 and 34 to 37)**

1. Disconnect the card wire from connector CN908 on the relay board on the back of the front panel assembly.
2. Remove the four screws **A'** attaching the CD / MD door cover on the front side of the front panel assembly. Remove the CD / MD door cover, then the LCD board unit.
3. Pull out the LCD lens from the four bosses on the LCD board unit.
4. Remove the LCD board and the lens cover in the direction of the arrow (The lens cover is attached with a double-sided tape).
5. Disconnect the sub board from connector CN913 on the LCD board.

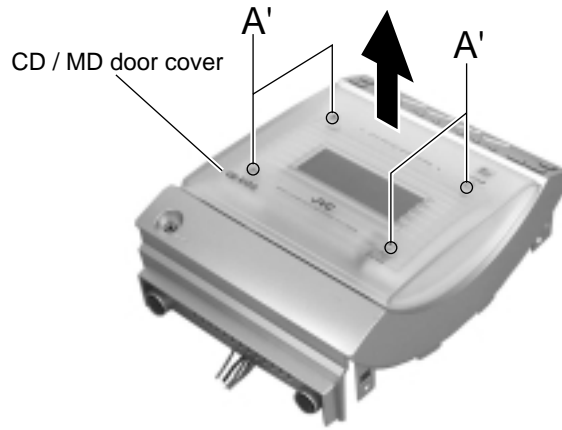


Fig.34

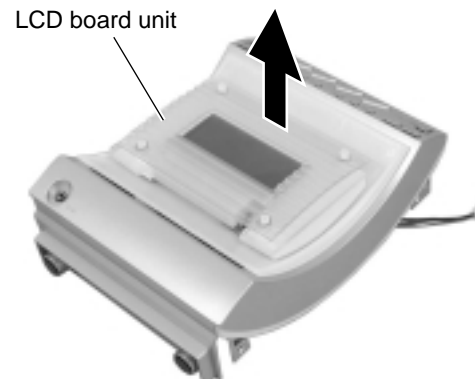


Fig.35

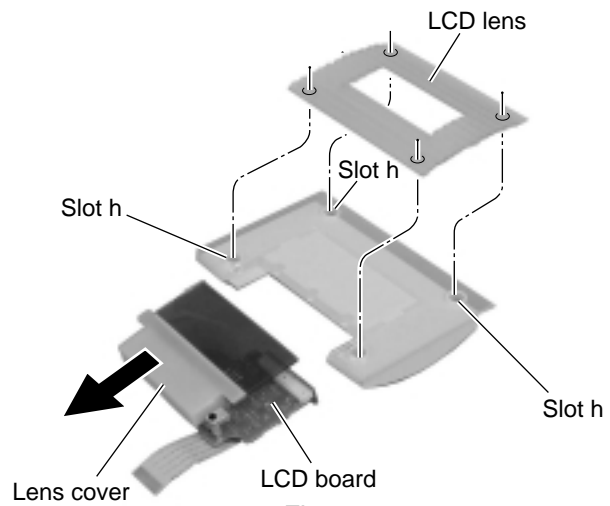


Fig.36

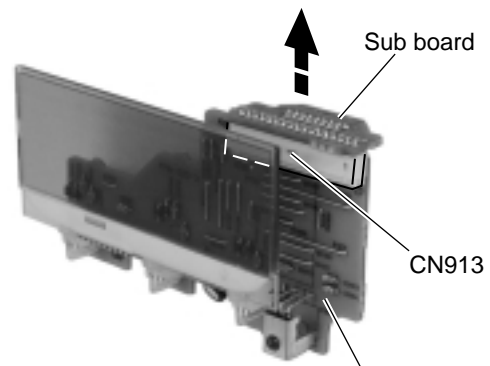
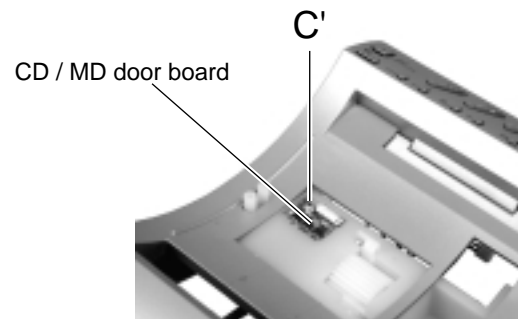
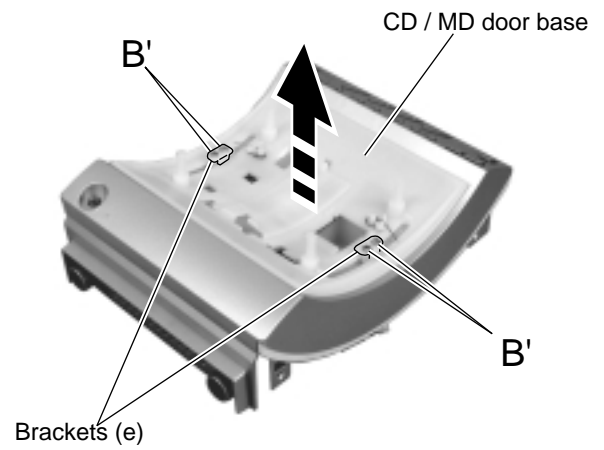


Fig.37

■ Removing the CD / MD door board (See Fig.38 and 39)

- Prior to performing the following procedure,remove the LCD board unit.
1. Remove the four screws **B'** attaching the two brackets (e). Remove the CD / MD door base and the two brackets (e).
 2. Remove the screw **C'** attaching the CD / MD door board.



Disassembly method

<Main body>

■Removing the main board (See Fig.1 and 2)

1. Turn over the body and disconnect the card wire from connector CN408 and the flexible wire from CN407 on the main board.
2. Remove the two screws A attaching the main board. Slide the main board in the direction of the arrow to release the two joints a with the single flame.
3. Solder part b on the pickup in the body. Disconnect the flexible harness from connector CN321 and CN451 on the underside of the main board. Then remove the main board.

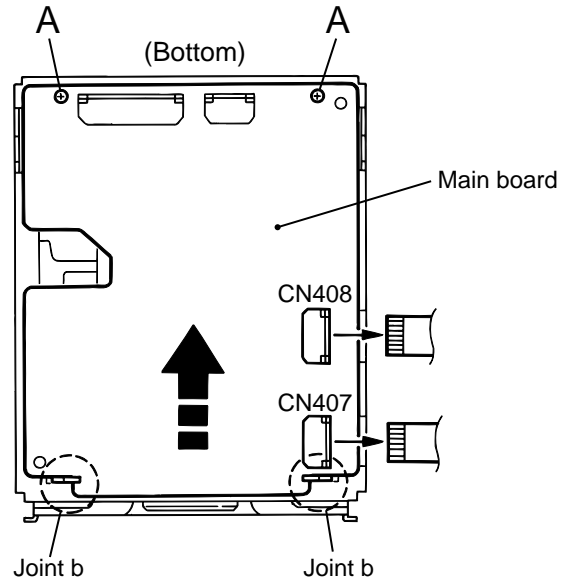


Fig.1

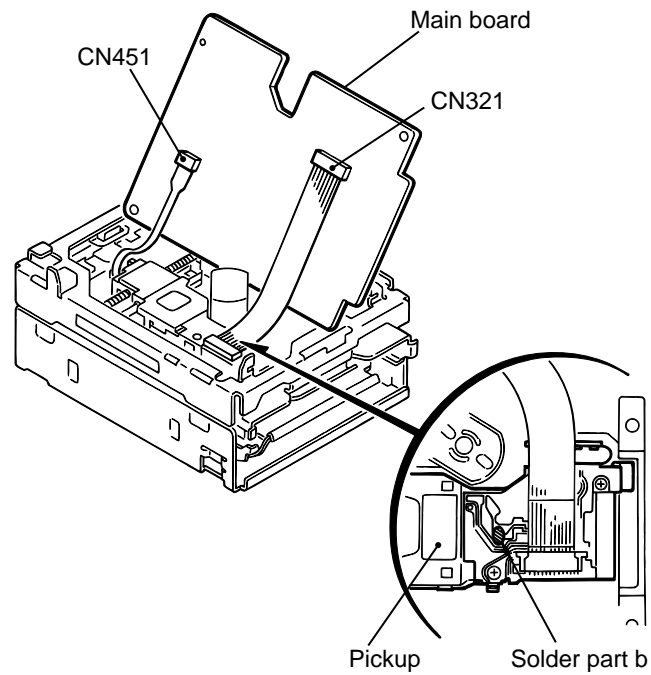


Fig.2

■Removing the mechanism cover (See Fig.3)

1. Remove the four screws B on both sides of the body.
2. Move the mechanism cover toward the front to disengage the front hook of the mechanism cover from the internal loading assembly (Joint c). Then remove the mechanism cover upward.

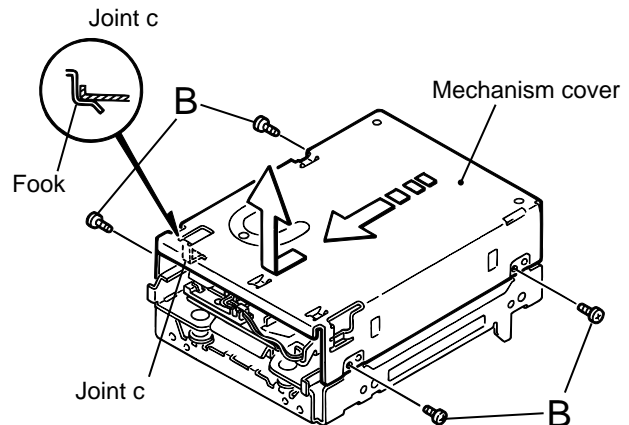
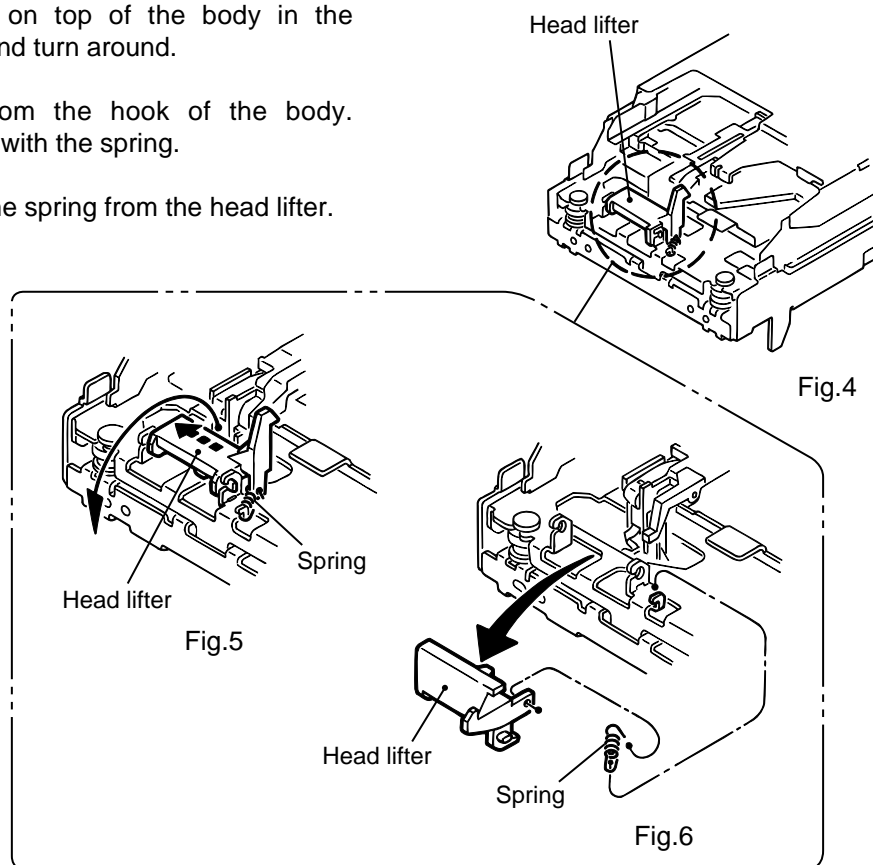


Fig.3

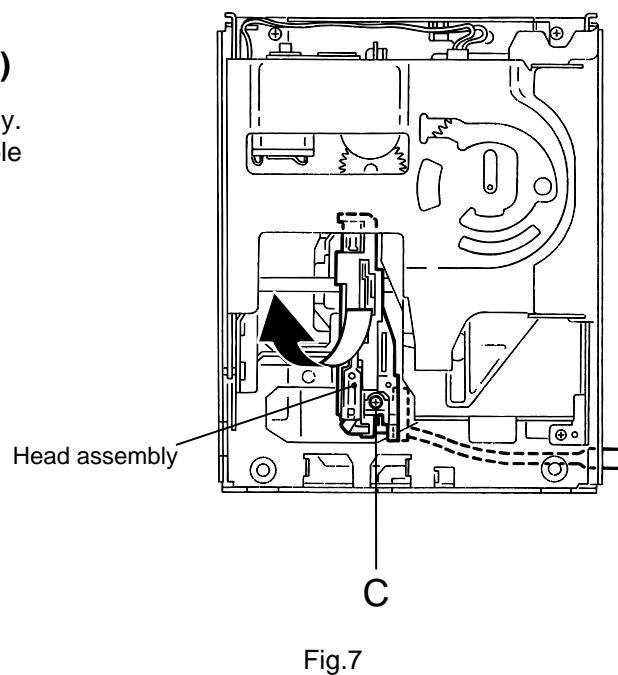
■Removing the head lifter (See Fig.4 to 6)

1. Move the head lifter on top of the body in the direction of the arrow and turn around.
2. Detach the spring from the hook of the body. Remove the head lifter with the spring.
3. If necessary, remove the spring from the head lifter.



■Removing the head assembly (See Fig.7)

1. Remove the screw C on the upper side of the body. Remove the head assembly while pulling the flexible harness from the body.



■Removing the Loading assembly
(See Fig.8 and 9)

Ref: The loading assembly, the traverse mechanism assembly and the single flame will be removable after removing the loading assembly from the body.

• Prior to performing the following procedure, remove the main board, the mechanism cover, the head lifter and the head assembly.

1. Remove the three screws D on the upper side of the body.
2. Move the loading assembly forward to disengage it from the traverse mechanism assembly (Joint d). Then remove it upward.
3. Remove the traverse mechanism assembly from the single flame.

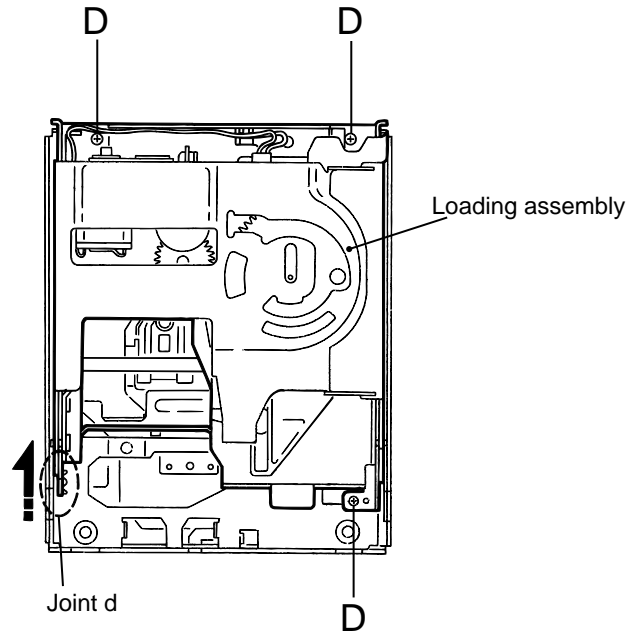


Fig.8

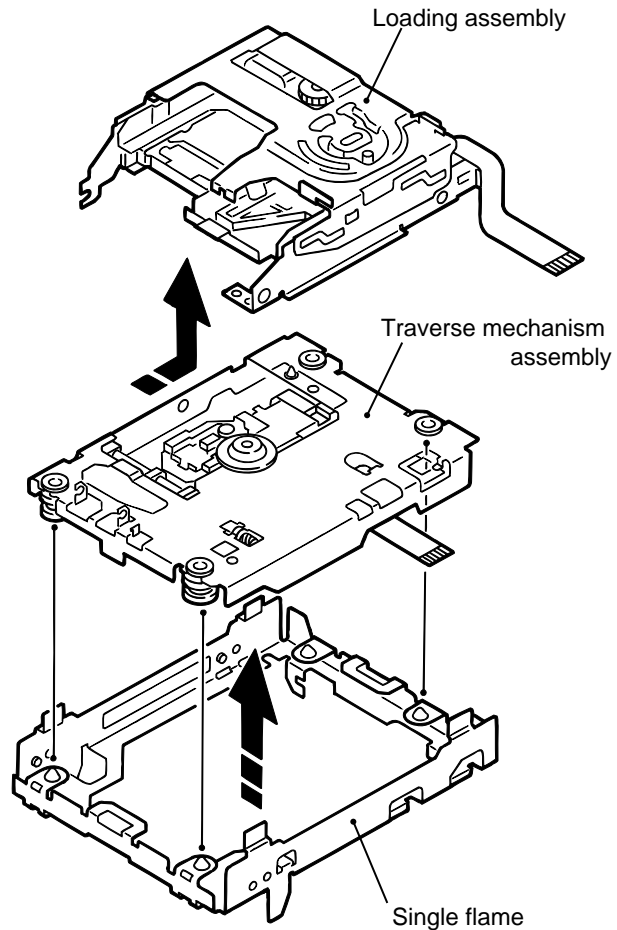


Fig.9

<Loading assembly>

■Removing the slide base (L) / (R)
(See Fig.10)

1. Remove the two screws E on the upper side of the loading assembly.
2. Remove the slide base (L) outward. (Release it from the joint bosses E.)
3. Remove the slide base (R) outward.

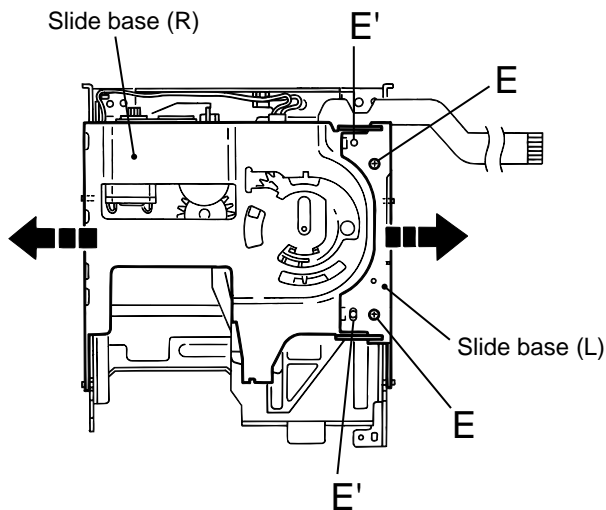


Fig.10

■Removing the loading mechanism assembly (See Fig.11)

1. Detach the loading mechanism assembly upward to release the four pins on both sides from the loading motor, paying attention to the part e of the loading mechanism base.

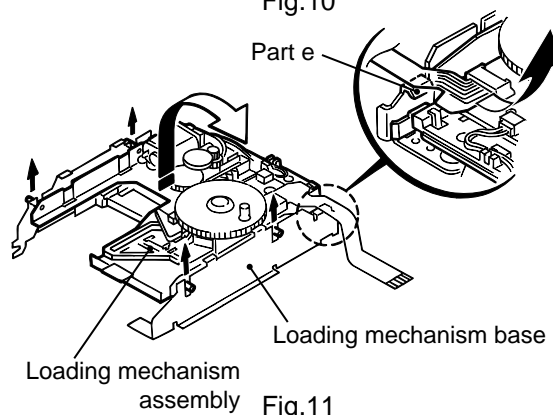


Fig.11

— Loading mechanism assembly —

■Removing the loading motor
(See Fig.12 and 13)

1. Disconnect the harnesses from the wire holder and from connector CN612 on the cam switch board.
2. Remove the screw F attaching the loading motor and release the joint f.
3. Remove the belt from the loading motor assembly.
4. Remove the two screws G attaching the loading motor.

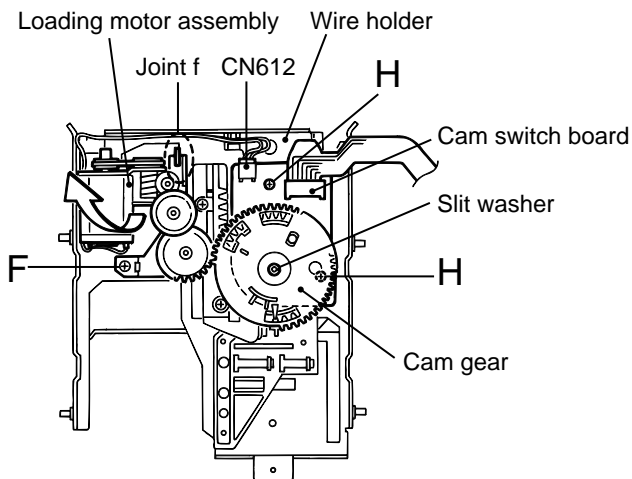


Fig.12

■Removing the cam gear and the cam switch board (See Fig.12)

1. Remove the slit washer attaching the cam gear and pull out the cam gear.
2. Disconnect the harness from the wire holder and from connector CN612 on the cam switch board.
3. Remove the two screws H and the clamp. Remove the cam switch board.

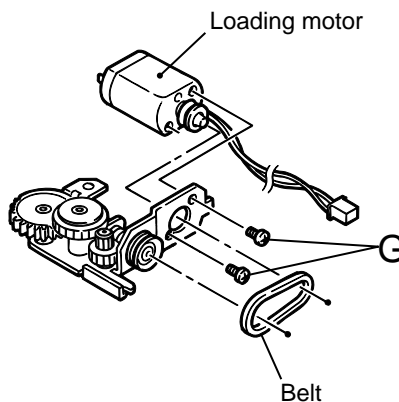


Fig.13

**■ Removing the cartridge holder assembly
(See Fig.14 and 15)**

1. Remove the two screws I on the upper side of the loading assembly.

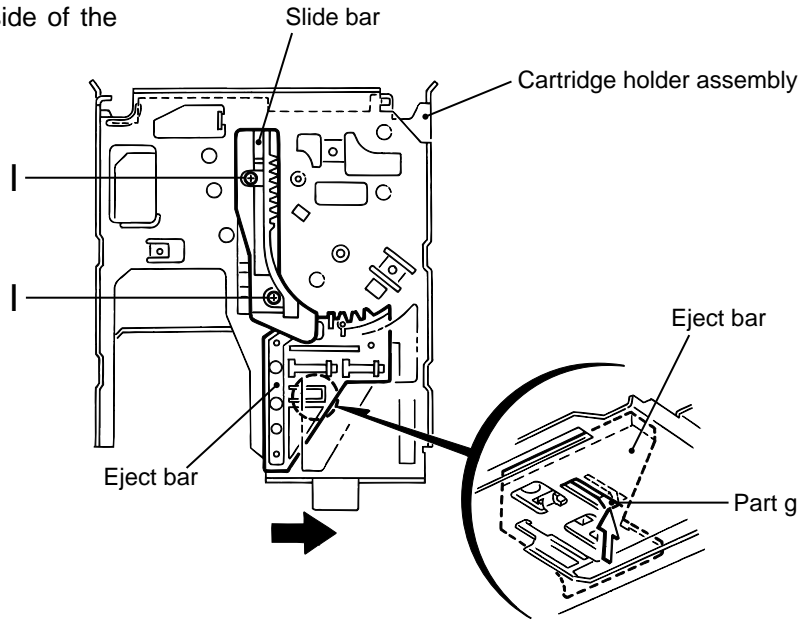


Fig.14

**■ Removing the slide bar and the eject bar
(See Fig.14 and 15)**

- Prior to performing the following procedure, remove the cartridge holder assembly.

1. Remove the slide bar upward.
2. Move the eject bar outward until it stops as shown in Fig.14. Push the convex part g on the bottom of the body and remove the eject bar from the chassis.

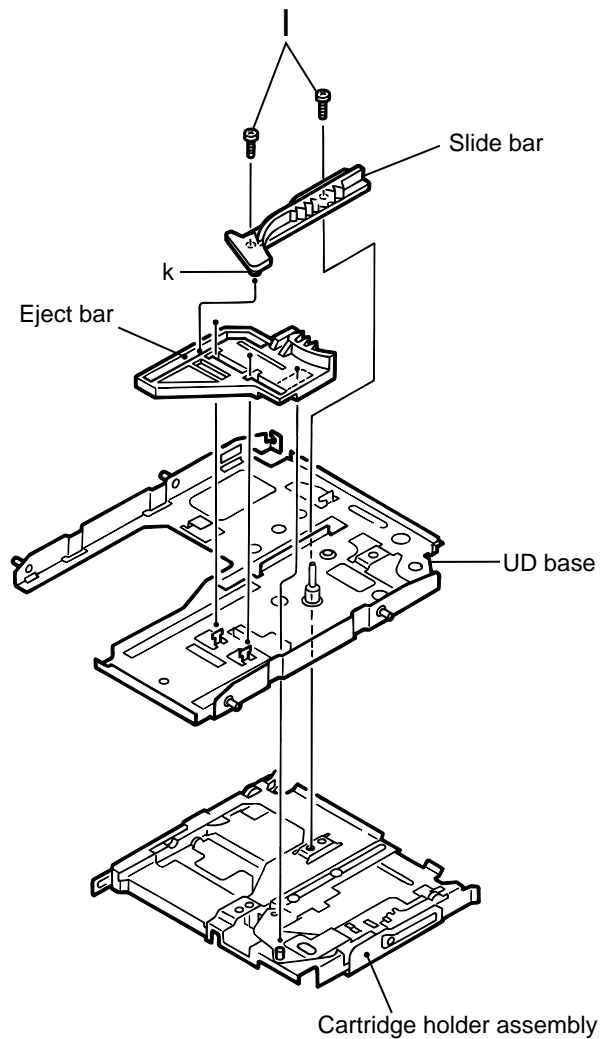


Fig.15

< Traverse mechanism assembly >

■ Removing the Insulators (See Fig.16)

1. Disengage the four insulators from the notches of the traverse mechanism chassis.

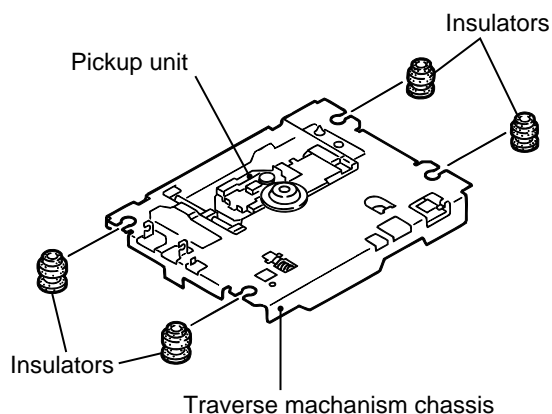


Fig.16

■ Removing the pickup unit (See Fig.17)

1. Turn over the traverse mechanism assembly and remove the screw J attaching the shaft holder (F).
2. Move the shaft inward and remove it from the shaft holder (R).
3. Detach the shaft side of the pickup unit upward and release the joint h with the pickup guide. Then remove the pickup unit with the shaft.

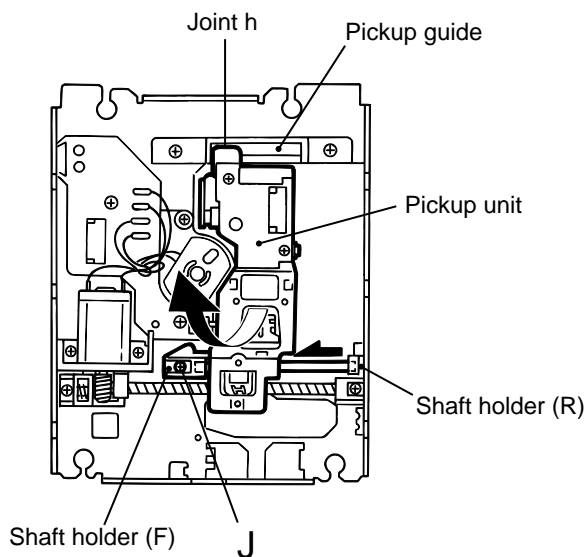


Fig.17

■ Removing the pickup (See Fig.18)

1. Draw out the shaft from the pickup.
2. Remove the two screws K attaching the rack spring.

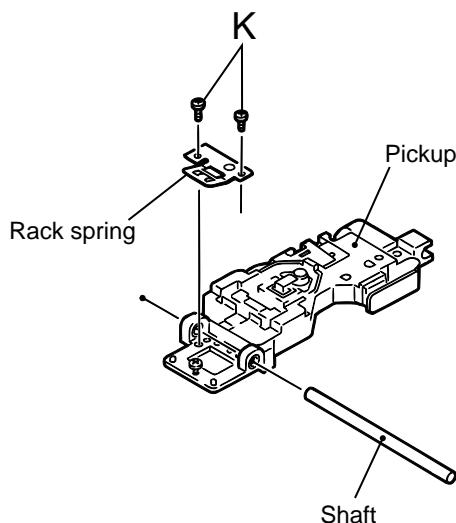


Fig.18

**■Removing the feed motor assembly
(See Fig.19-1, 19-2)**

- It is not necessary to remove the pickup unit.
1. For the white and black harnesses extending from the feed motor assembly, unsolder the soldering i on the traverse mechanism board.
 2. Remove the two screws L attaching the feed motor assembly.
 3. Remove the two screws N attaching the feed motor bracket.

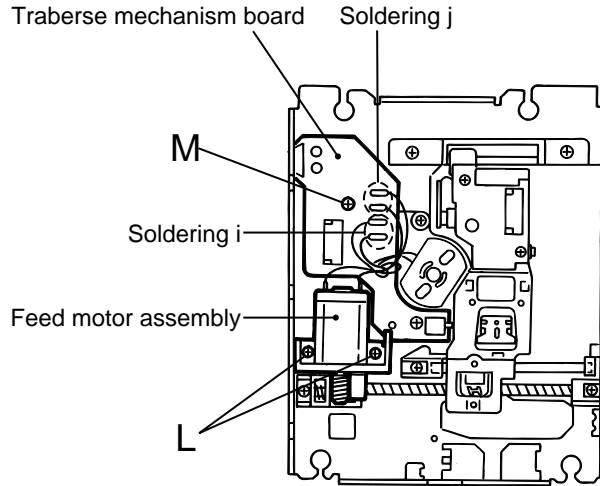


Fig.19-1

■Removing the traverse mechanism board (See Fig.19-1)

- Prior to performing procedure, remove the feed motor assembly.
1. For the red and black harnesses extending from the spindle motor, unsolder the soldering j on the traverse mechanism board.
 2. Remove the screw M attaching the traverse mechanism board.

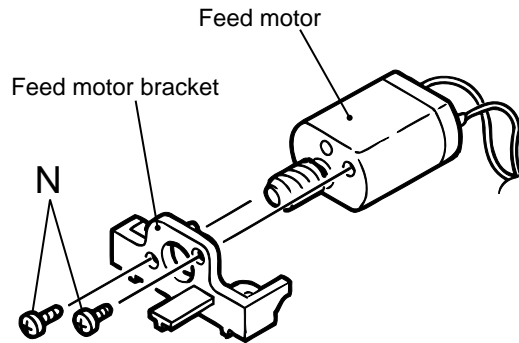


Fig.19-2

<Reattaching the loading assembly>

1. Reattach the eject bar to the UD base.
(Fig.15 and 20)
2. Reattach the slide bar to the loading mechanism chassis while fitting the boss marked k to the eject bar slot. (Fig.20)
3. Slide the slide bar and the eject bar in the direction of the arrow in Fig.20 and reattach the cartridge holder assembly using the two screws I.
(Fig.20 and 21)

ATTENTION: Make sure the pin of the eject lever marked l is fitted to the slot of the eject bar marked m at the bottom of the loading mechanism chassis after moving the eject lever and the loading slider of the cartridge holder assembly in the direction of the arrow.

(Refer to Fig.21)

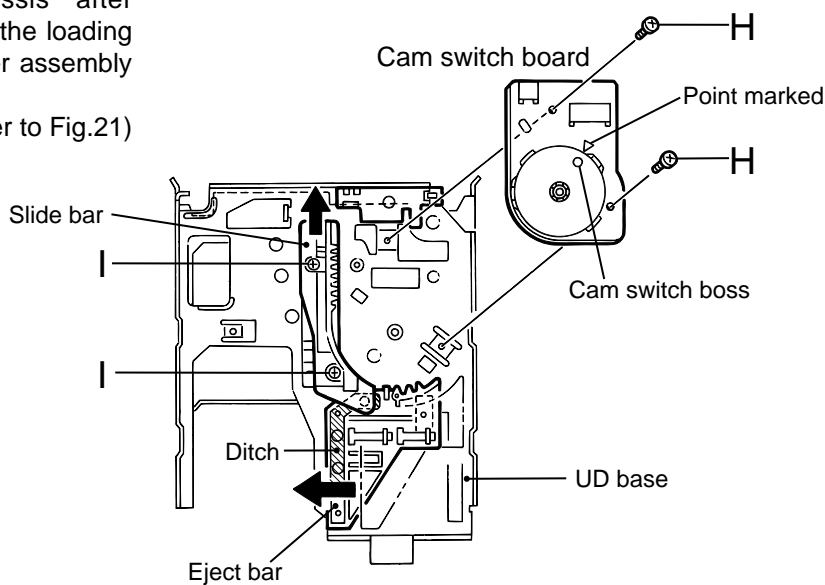


Fig.20

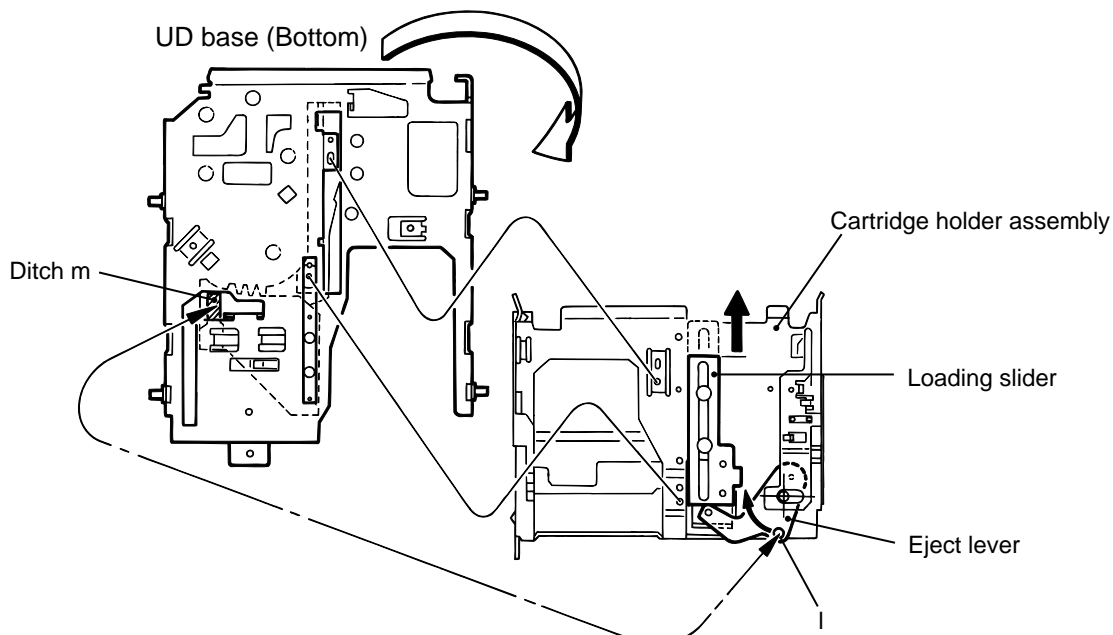


Fig.21

4. Reattach the wire holder to the UD base while engaging the UD base hook marked u to the wire holder slot marked t (At the same time, the boss on the reverse side of the wire holder is fitted to the UD base round hole).
5. Reattach the cam switch board using the two screws H. (Fig.22)
6. Turn the cam switch to bring the boss to the point marked Δ on the cam switch board. Reattach the cam gear using a slit washer while fitting the cam gear slot to the cam switch boss. (Fig.22)

ATTENTION: When reattaching the cam gear, the cam switch boss should be fitted to the cam gear slot, and the triangle mark of the cam gear should be aligned to the hole of the eject bar as shown in Fig.22.

7. Reattach the loading motor assembly, using the screw F. Connect the harness extending from the loading motor to connector CN612 on the switch board and fix it with the wire holder. (Fig.22)

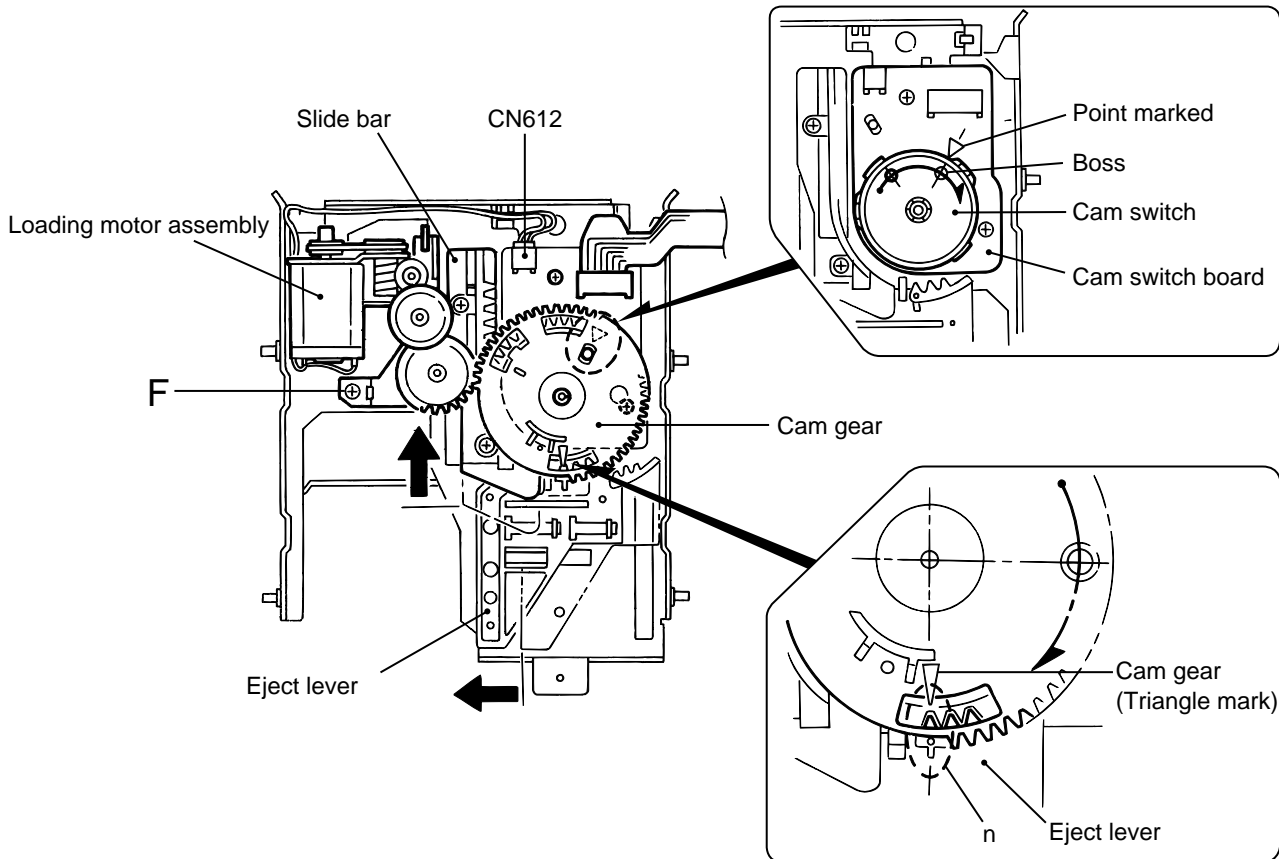


Fig.22

8. Reattach the UD base while engaging the four pins on both sides of the UD base to the notches of the loading mechanism base and placing the edge (marked e') of the cartridge holder assembly under the hook e of the loading mechanism base. (Fig.23)

9. Reattach the slide base (R) while fitting the two pins on another side of the UD base to the slots of the slide base (R). (Fig.24)

ATTENTION: Fit the part v of the slide base (R) to the part w on the inward side of the cam gear rib. (Fig.25)

10. Reattach the slide base (L) on the slide base (R) while fitting the two pins on another side of the UD base to the slots of the slide base (L) (Fig.25). Make sure the two slots of the slide base (L) are fitted to the two bosses marked E' and tighten the two screws E. (Fig.26)

Ref: To expedite the work, bring up the UD base slightly when fitting each pin to the appropriate notch.

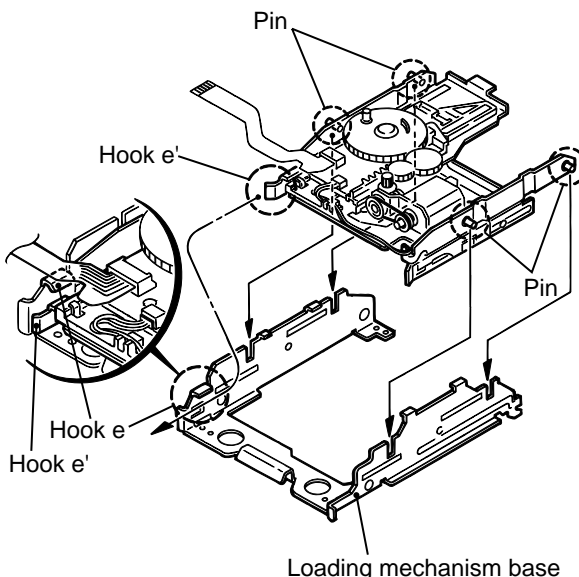


Fig.23

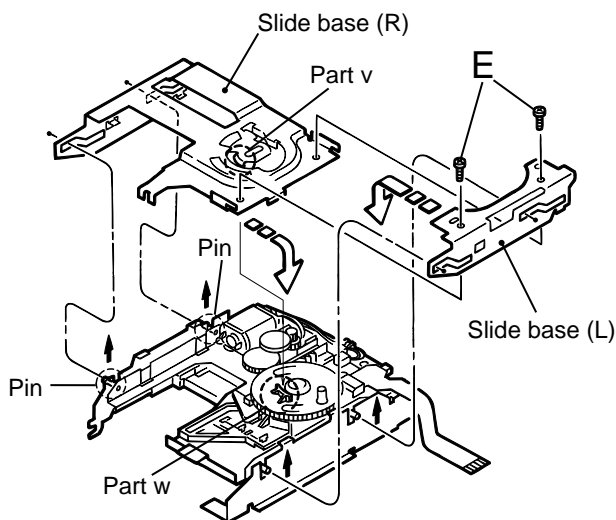


Fig.24

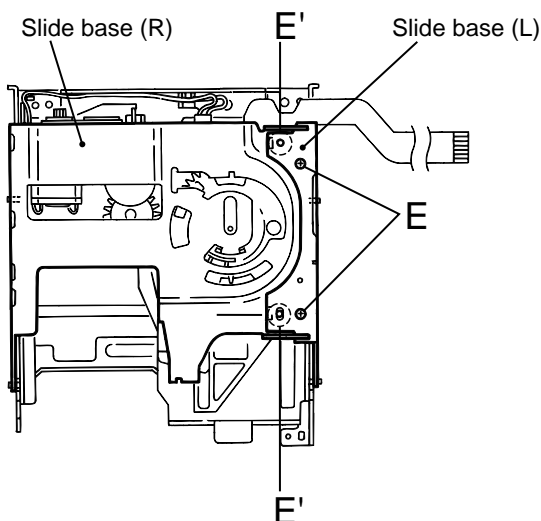


Fig.26

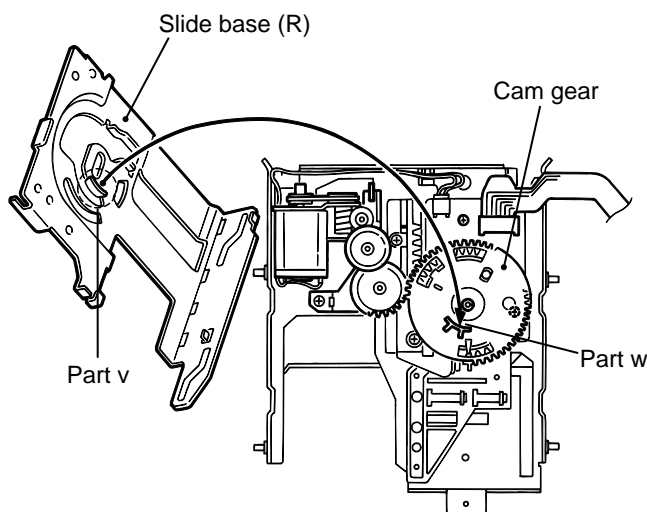


Fig.25

Adjustment Method (CD/MD section)

1. Jigs and test instruments

- Laser power meter
- Laser power meter sensor (or disk sensor)
- Premastered disk (MRG-1018 or TGYS1)
- Recordable disk (MDW-74/AU1)

2. Adjustment and check items

1) Indications in the modes that all LCD's are on

2) CD section

- (1) Indication of the C1 error
- (2) Cancel of the C1 error indication

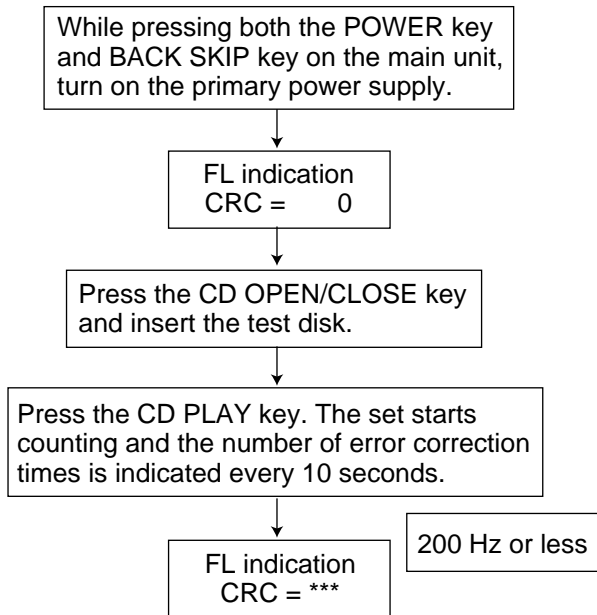
3) MD section

- (1) Setup of the TEST MODE 1
- (2) Initialization of the EEPROM
- (3) Adjustment of the laser power
- (4) Adjustment of the disk
- (5) Setup of the TEST MODE 2
- (6) Indication of variation in the pickup adjustment value
- (7) Indication of the C1 error
- (8) Cancel of the TEST MODE

3. Adjustment and check method

1) CD section

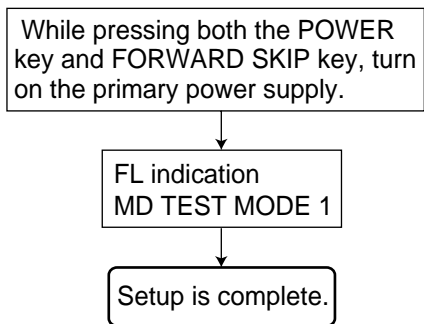
- (1) Indication of the C1 error



- (2) Cancel of the C1 error indication
To cancel the C1 error indication, cut off the power supply.

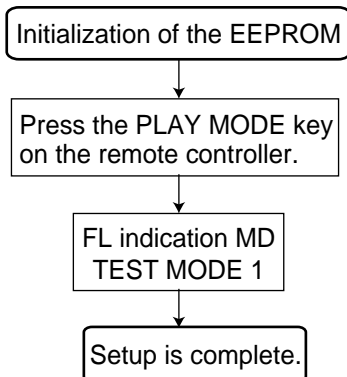
2) MD section

(1) Setup of the TEST MODE 1

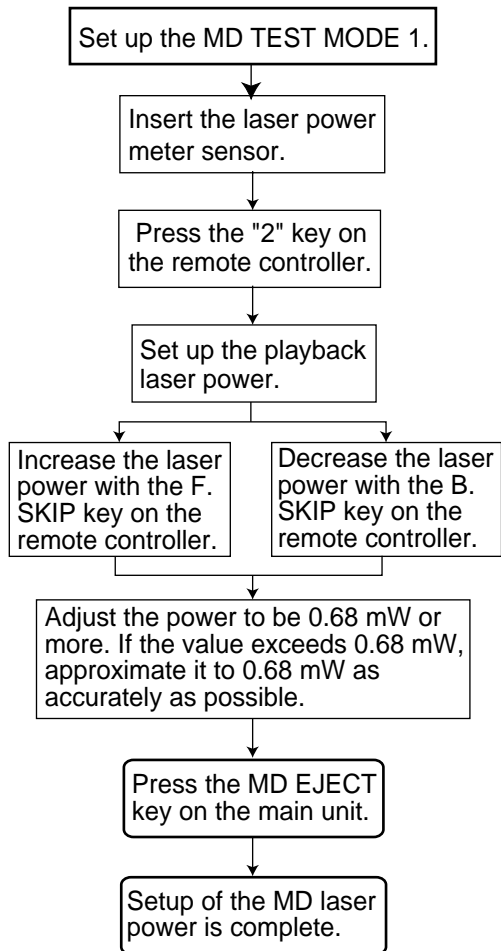


(2) Initialization of the EEPROM

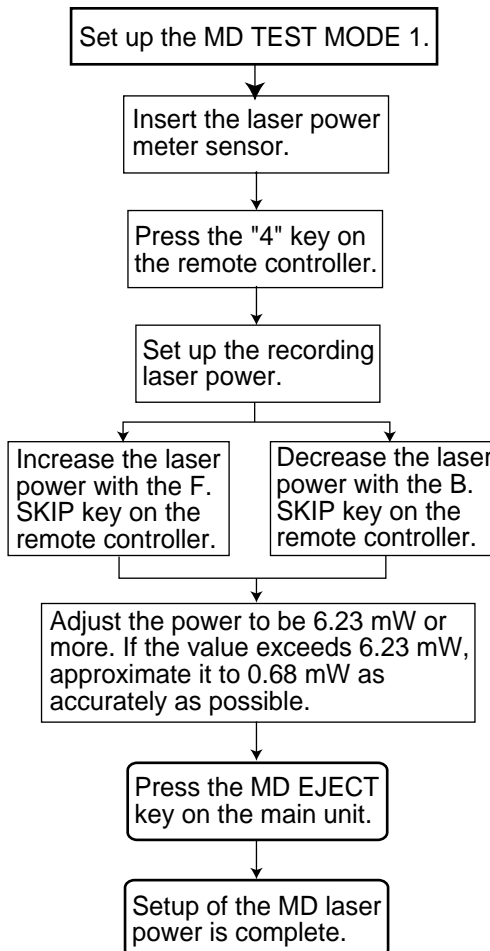
(The EEPROM can be initialized on the precondition that the setup of the TEST MODE 1 is complete. After setup of the TEST MODE 1, proceed to the following operations with the remote controller*.)
 * For EJECT operation, use the EJECT key on the main unit.



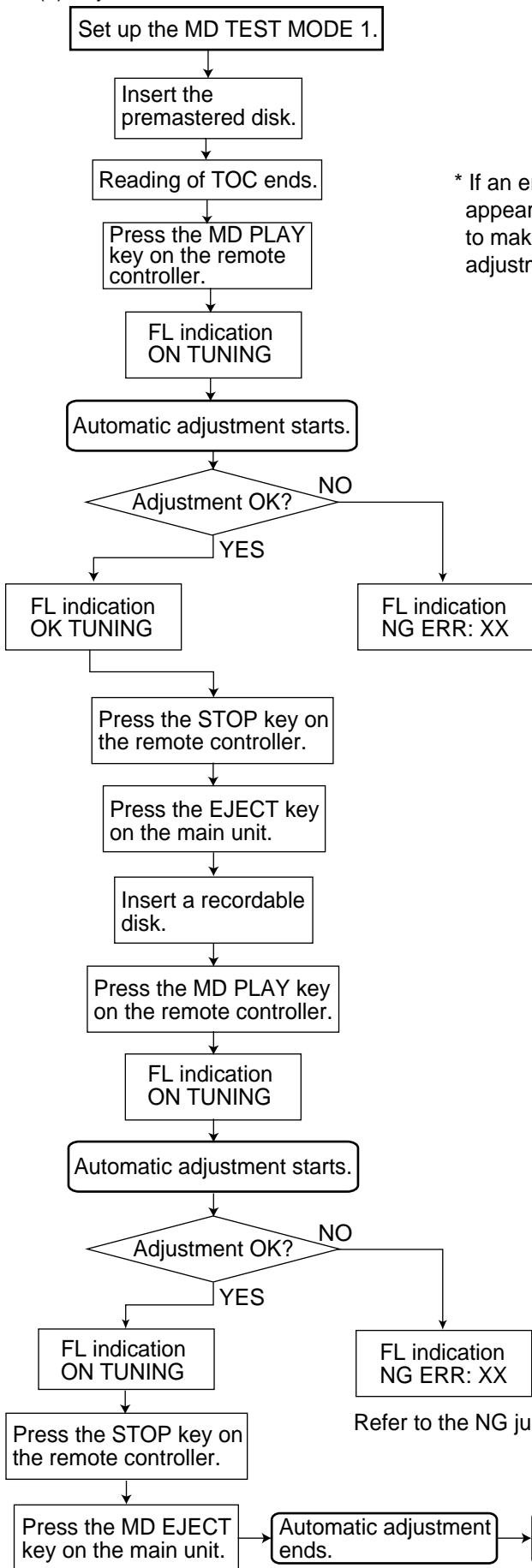
(3) Adjustment of the playback laser power



(4) Adjustment of the recording laser power



(5) Adjustment of the disk



* If an error indication of "ERR:07" appears at the first adjustment, try to make a fresh start of the same adjustment.

XX: Refer to the NG judgment code table.

NG Judgment Code Table

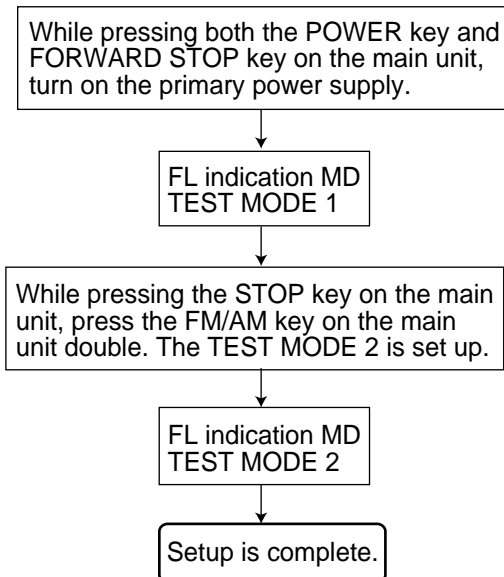
Code	NG item in adjustment
00	Automatic adjustment is incomplete.
01	Rest switch detection
02	Focus-on
03	EF balance, tracking offset adjustment (Pit area)
04	ABCD level (I-V resistance) adjustment (Pit area)
05	Focus servo AGC (Pit area)
06	Tracking servo AGC (Pit area)
07	Focus bias adjustment (Pit area)
08	EF balance, tracking offset adjustment (GRV area)
09	ABCD level (I-V resistance) adjustment (GRV area)
0A	Focus servo AGC (GRV area)
0B	Tracking servo AGC (GRV area)
0C	Focus bias adjustment (GRV area)
0D	Room temperature measurement
0E	Write in EEPROM
10	(Focus, TRK, SLED) AGC data read time-out
20	Pit area spindle lock
30	Groove area spindle lock
XX	Automatic adjustment is complete.

Refer to the NG judgment code table.

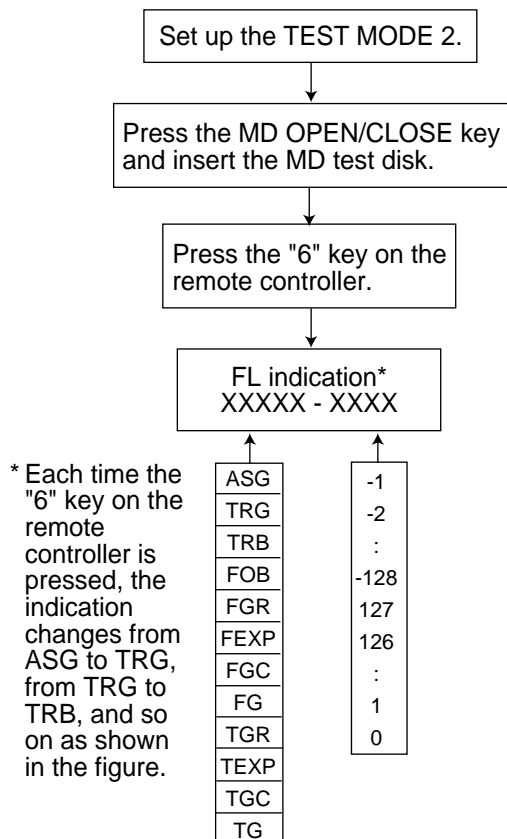
For investigating the mode in which an error occurred during the disk adjustment, freeze the set in the mode by pressing the proper key (refer to the table on the right) on the remote controller before cancelling the TEST MODE 1.

Key to press	Mode
SLEEP key (Remote controller)	FOCUS SEARCH
"6" key (Remote controller)	PIT ROUGH SERVO
"7" key (Remote controller)	GROOVE ROUGH SERVO
"8" key (Remote controller)	TRACKING ON
"9" key (Remote controller)	TRACKING OFF
STOP key (Remote controller)	STOP
EJECT key (Main unit)	EJECT

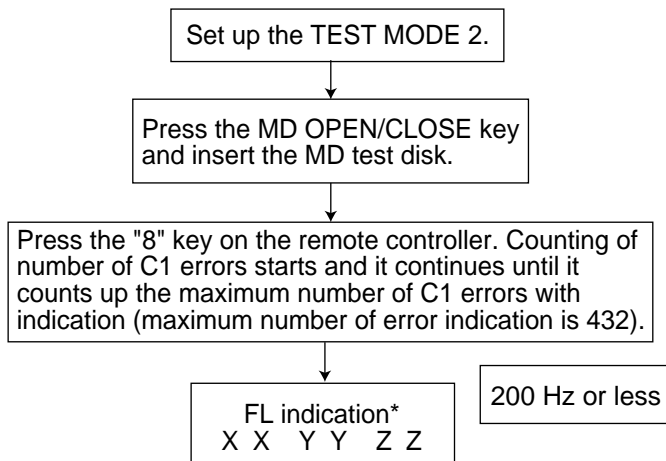
(6) Setup of the TEST MODE 2



(7) Indication of variation in the pickup adjustment value



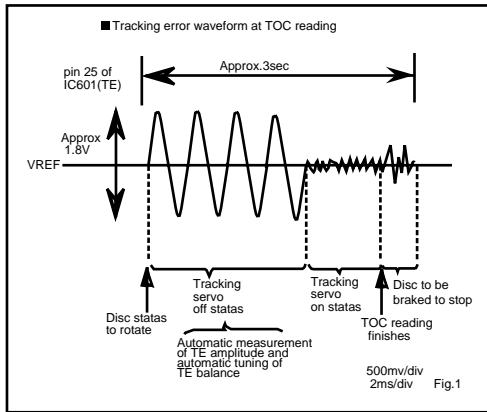
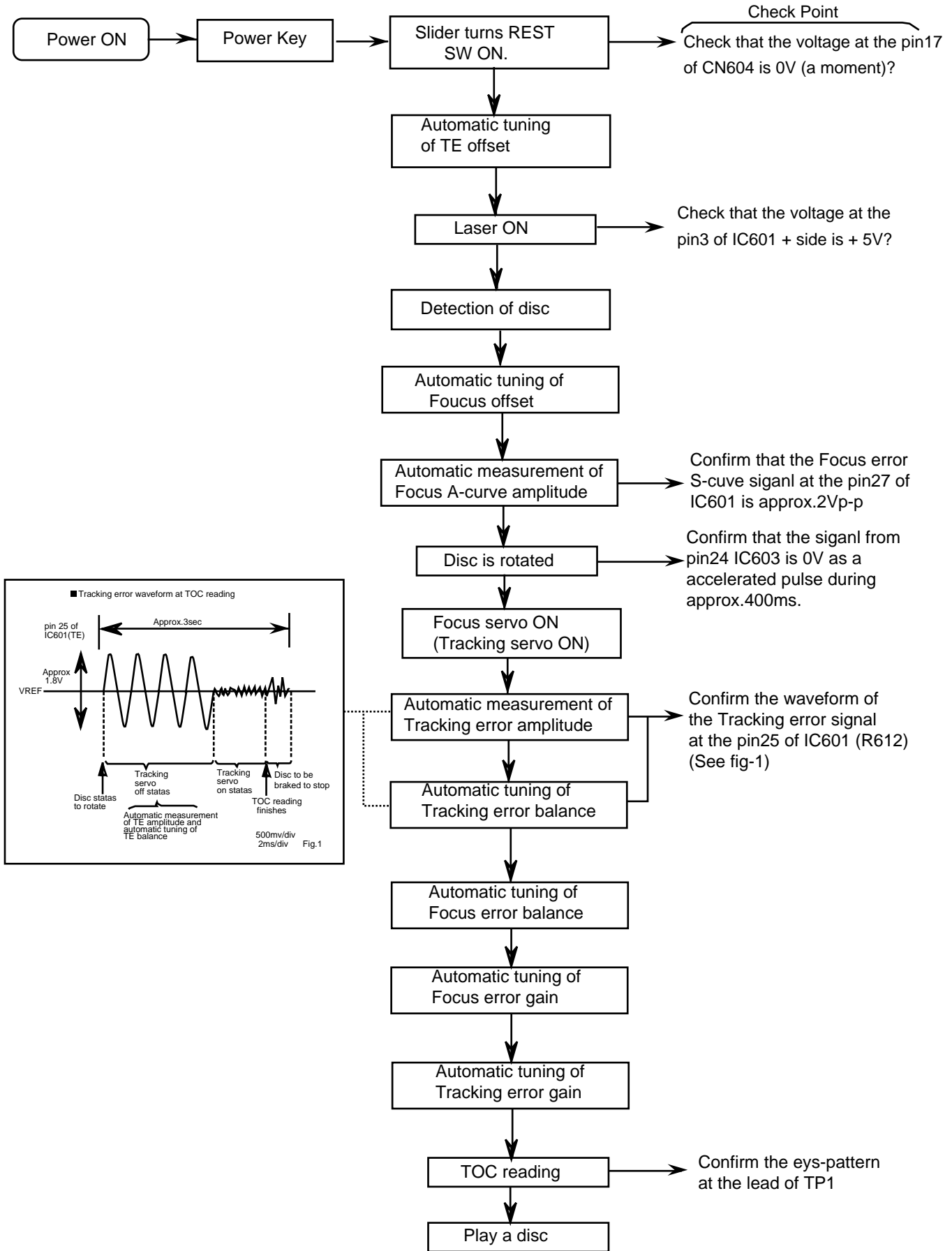
(8) Indication of the C1 error



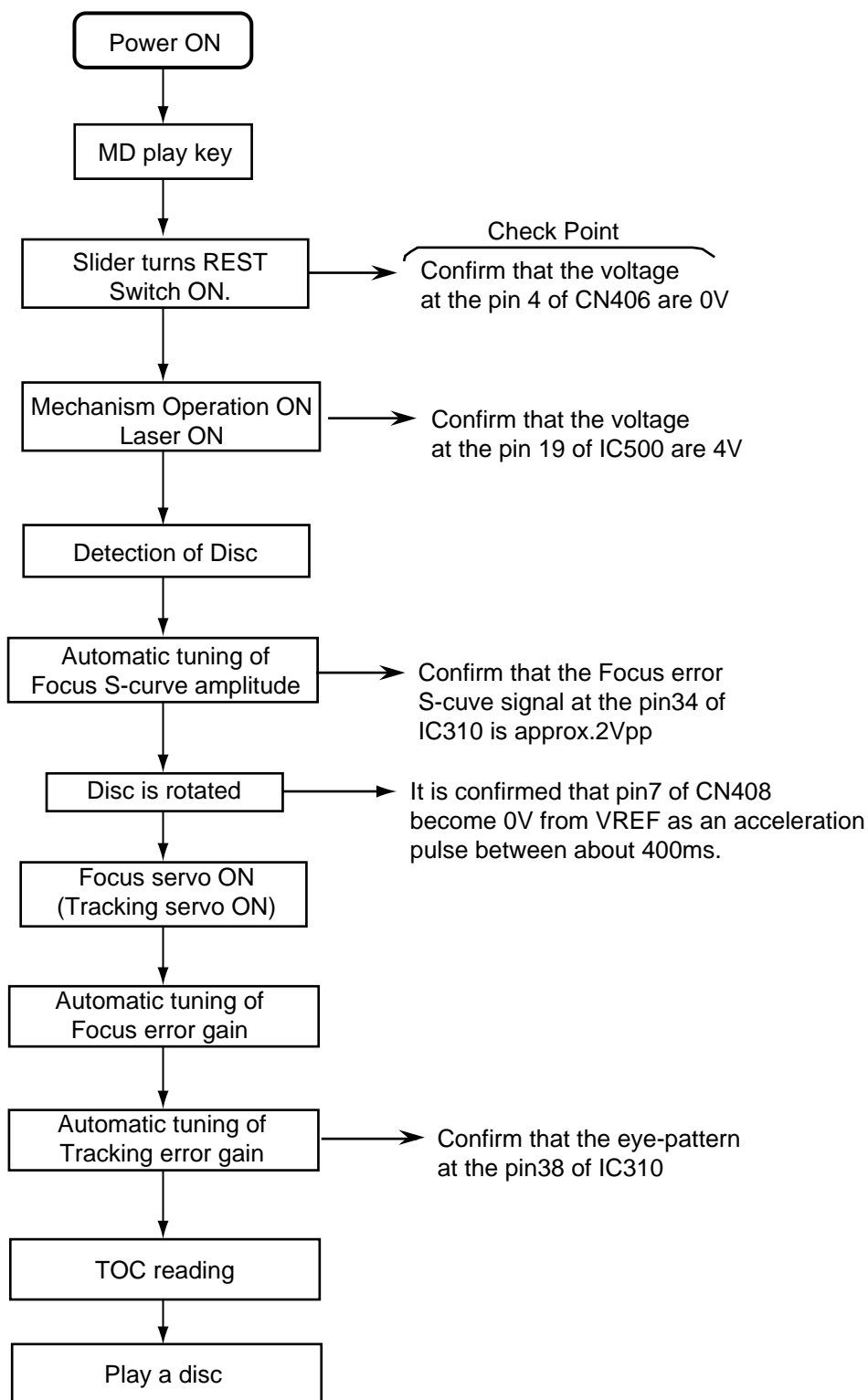
(9) Cancel of the TEST MODE
(The cancel operation is common to the TEST MODE 1 and 2.)

Cut off the primary power supply to cancel the TEST MODE.

Flow of functional operation until TOC read (CD)

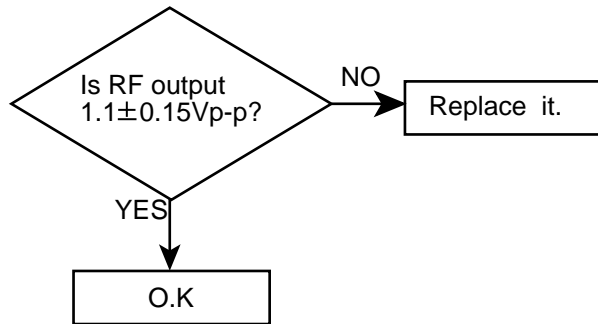


Flow of functional operation until TOC read (MD)



Maintenance of laser pickup

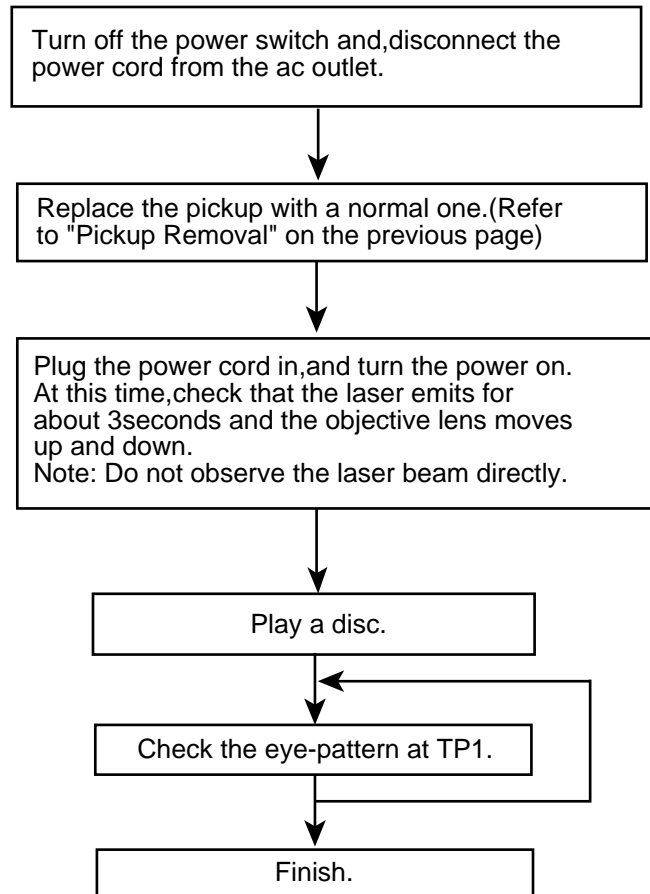
- (1) Cleaning the pick up lens
 Before you replace the pick up, please try to clean the lens with a alcohol soaked cotton swab.
- (2) Life of the laser diode (Fig.1)
 When the life of the laser diode has expired, the following symptoms wil appear.
 - (1) The level of RF output (EFM output:ampli tude of eye pattern) will below.



(Fig.1)

- (3) Semi-fixed resistor on the APC PC board
 The semi-fixed resistor on the APC printed circuit board which is attached to the pickup is used to adjust the laser power. Since this adjustment should be performed to match the characteristics of the whole optical block, do not touch the semi-fixed resistor.
 If the laser power is lower than the specified value, the laser diode is almost worn out, and the laser pickup should be replaced.
 If the semi-fixed resistor is adjusted while the pickup is functioning normally, the laser pickup may be damaged due to excessive current.

Replacement of laser pickup



Maintenance of MD pickup

1. Cleaning of pickup lens

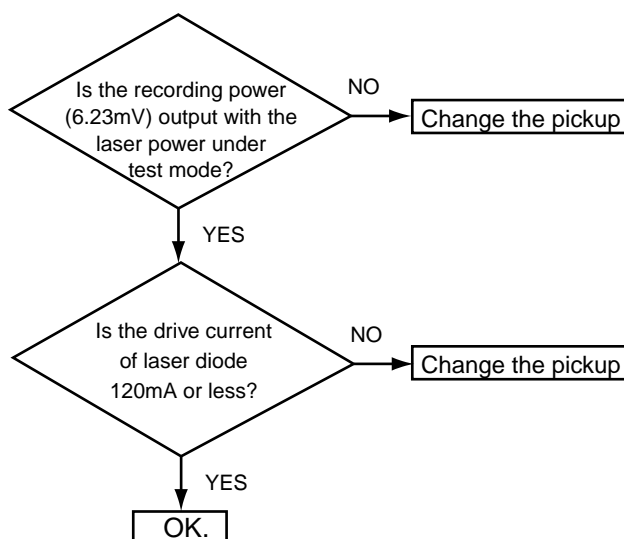
- (1) Prior to changing the pickup, clean the pickup lens.
- (2) For cleaning the lens, use the following cotton swab after mearsing it in alcohol.

Product No. JCB-B4; Manufacturer;Nippon Cotton Swab

2. Confirmation of the service life of laser diode when the service life of the laser diode has been exhausted, the following symptoms will appear.

- (1) Recording will become impossible.
- (2) The RF output (EFM output and eye pattern amplitude) will become lower.
- (3) The drive current required for light emitting of laser diode will be increased.

Confirm the service life according to the following flow chart:

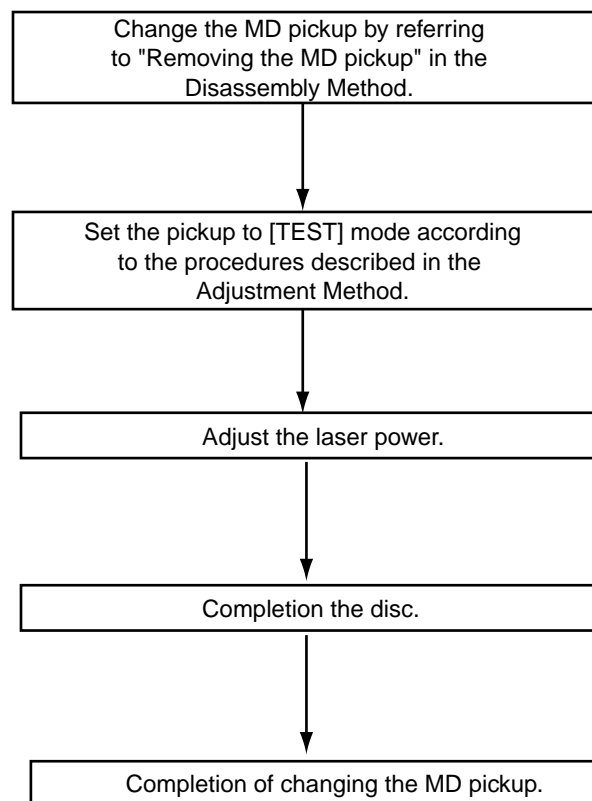


3. Method of measuring the drive current of laser diode

When the voltage measured at TP ILCC (Q301 emitter) and +5V (Q303 emitter) of the MD servo P.C. board (ENX-0223) have become 600mV or over, the service life of the laser diode is judged to have been exhausted.

[Caution] When TP ILCC (Q301 collector) and +5V (Q303 emitter) have been is short circuit on such an occasion, then the laser diode will be broken. Therefore, take utmost care in handling the MD pickup.

Procedures of changing the MD pickup



Since this system is designed to perform magnetic recording, the laser power ten times or over of the conventional MD player will be output. Therefore, be sure to perform not only adjustment and operation of this system so carefully as not to directly look at the laser beam or touch on the body.

4. Semi-solid state resistors on the APC P.C. board

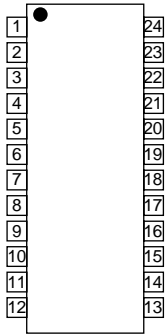
The semi-solid state resistor on the APC P.C.board attached to the pickup is used for adjusting the laser power. Since these resistor should be adjusted in pair according to the characteristics of the optical block, be sure not to touch on the resistors.

Since the service life of the laser diode will be exhausted when the laser power is low, it is necessary to change the pickup. Meanwhile, do not pickup. Otherwise, the pickup will be damaged due to over current.

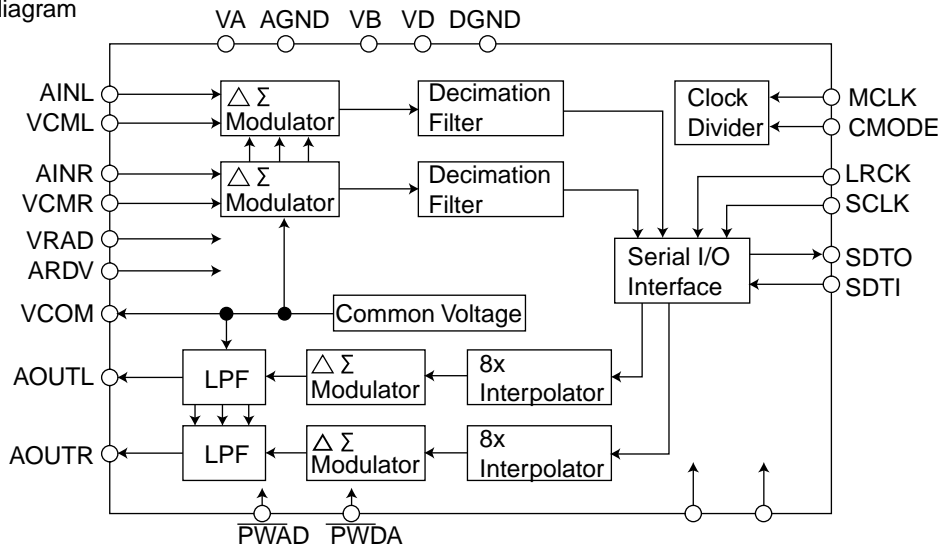
Description of major ICs

■ AK4519VF-X (IC480) : A / D D / A converter

1.Pin layout



2.Block diagram

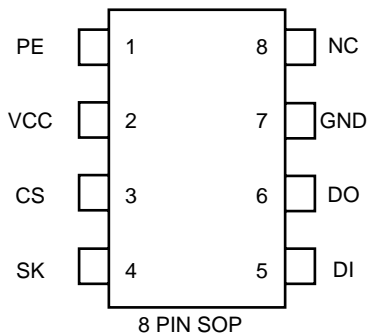


3.Pin Function

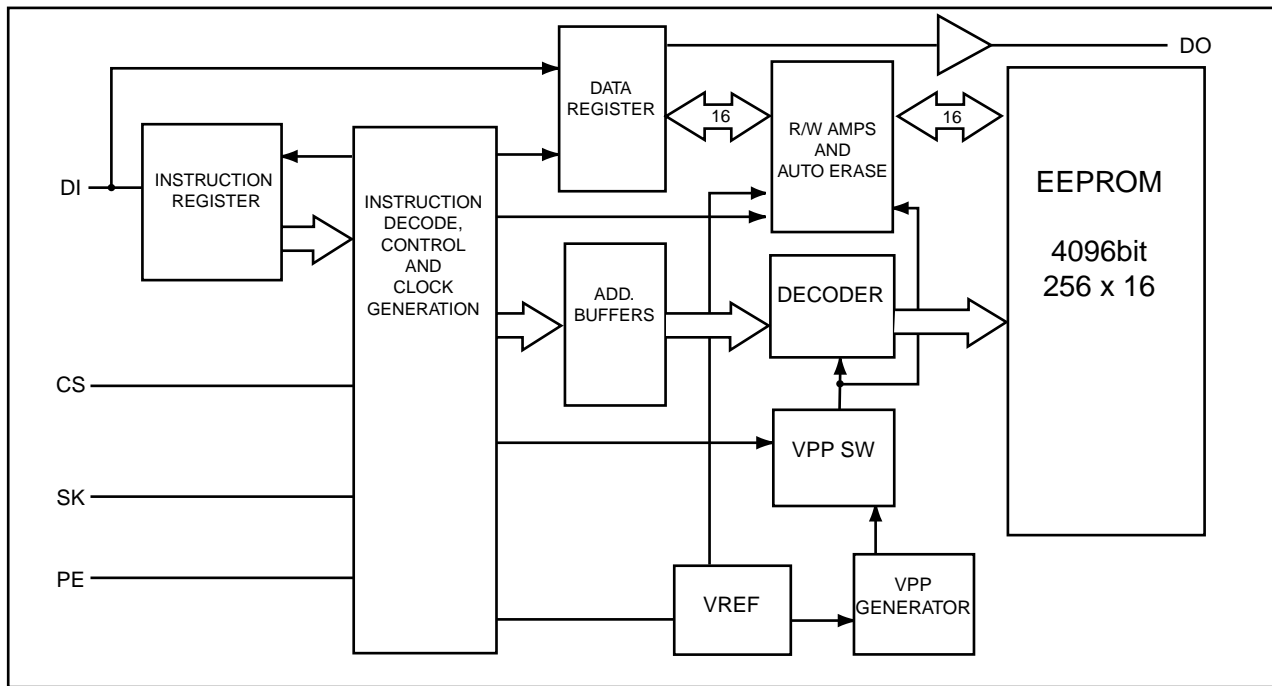
Pin NO.	Symbol	I/O	Function
1	VRDA	I	Voltage Reference Input Pin for DAC, VA
2	VRAD	I	Voltage Reference Input Pin for ADC, VA
3	AINR	I	Rch Analog Input Pin
4	VCMR	O	Rch Common Voltage Output Pin, 0.45xVA
5	VCML	O	Lch Common Voltage Output Pin, 0.45xVA
6	AINL	I	Lch Analog Input Pin
7	PWAD	I	ADC Power-Down Mode Pin "L":Power Down
8	PWDA	I	DAC Power-Down Mode Pin "L":Power Down
9	MCLK	I	Master Clock Input Pin
10	LRCK	I	Input/Output Channel Clock Pin
11	SCLK	I	Audio Serial Data Clock Pin
12	SDTO	O	Audio Serial Data Output Pin
13	DGND	-	Digital Ground Pin
14	VD	-	Digital Power Supply Pin
15	SDTI	I	Audio Serial Data Input Pin
16	CMODE	I	Master Clock Select Pin
17	DEM1	I	De-emphasis Frequency Select Pin
18	DEM0	I	De-emphasis Frequency Select Pin
19	AOUTL	O	Lch Analog Output Pin
20	AOUTR	O	Rch Analog Output Pin
21	VCOM	O	Common Voltage Output Pin, 0.45xVA
22	AGND	-	Analog Ground Pin
23	VB	-	Substrate Pin
24	VA	-	Analog Power Supply Pin

■ AK93C65AF-X (IC590) : EEPROM

1.Pin layout



2.Block diagram



3.Pin function

Pin no.	Symbol	Function
1	PE	Program enable (With built-in pull-up resistor)
2	VCC	Power supply
3	CS	Chip selection
4	SK	Cereal clock input
5	DI	Cereal data input
6	DO	Cereal data output
7	GND	Ground
8	NC	No connection

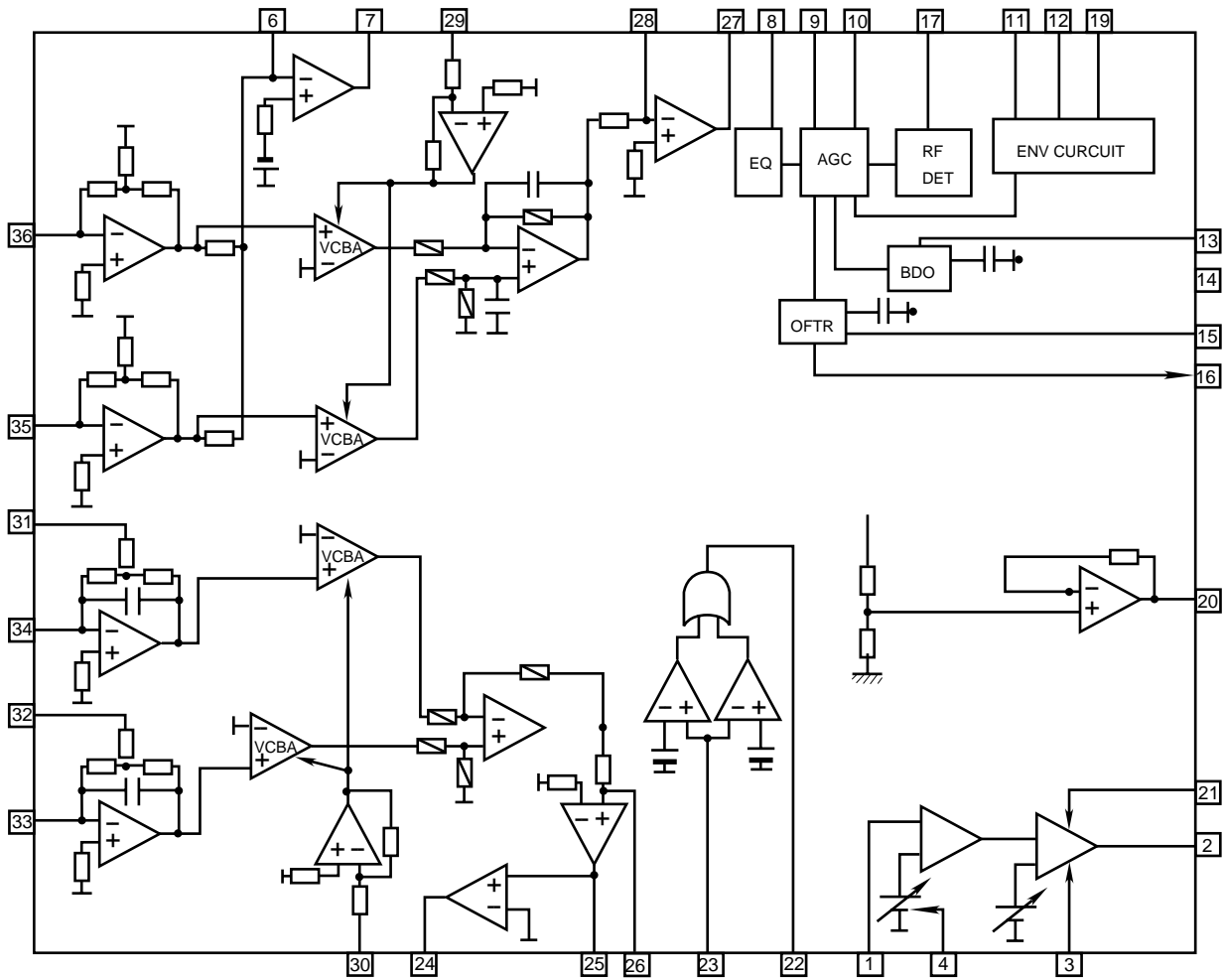
NOTE : The pull-up resistor of the PE pin is about 2.5M Ω (VCC=5V)

■ AN8806SB-W (IC601) : RF&Servo AMP

1.Pin layout

PD	1	36	PDAC
LD	2	35	PDBD
LDP	3	34	PDF
VCC	4	33	PDE
RF-	5	32	PDER
RF OUT	6	31	PDFR
RF IN	7	30	TBAL
C.AGC	8	29	FBAL
ARF	9	28	EF-
C.ENV	10	27	EF OUT
C.EA	11	26	TE-
CS BDO	12	25	TE OUT
BDO	13	24	CROSS
CS BRT	14	23	TE BPF
OFTR	15	22	VDET
/NRFDET	16	21	LD OFF
GND	17	20	VREF
	18	19	ENV

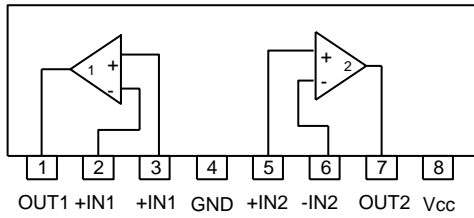
2.Block diagram



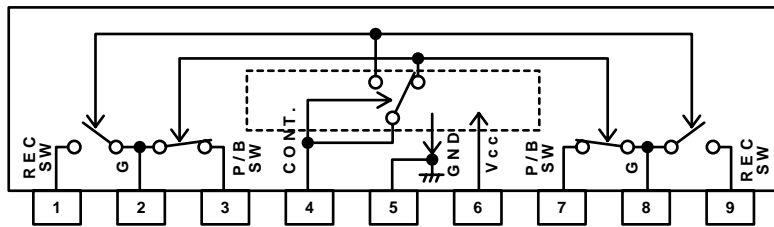
3. Pin function

Pin No.	Symbol	I/O	Function
1	PD	I	APC amp input terminal
2	LD	O	APC amp output terminal
3	LD ON	I	APC ON/OFF control terminal
4	LDP	--	Connect to ground
5	VCC	--	Power supply
6	RF-	I	Inverse input pin for RF amp
7	RF OUT	O	RFamp output
8	RF IN	I	RF input
9	C.AGC	I/O	Connecting pin of AGC loop filter
10	ARF	O	RF output
11	C.ENV	I/O	A capacitor is connected to this terminal to detect the envelope of RF signal
12	C.EA	I/O	A capacitor is connected to this terminal to detect the envelope of RF signal
13	CS BDO	I/O	A capacitor is connected to detect the lower envelope of RF signal
14	BDO	O	BDO output pin
15	CS BRT	I/O	A capacitor is connected to detect the lower envelope of RF signal
16	OFTR	O	Of-track status signal output
17	/NRFDET	O	RF detection signal output
18	GND	--	Ground
19	ENV	O	Envelope output
20	VREF	O	Reference voltage output
21	LD OFF	--	Connect to ground
22	VDET	O	Vibration detection signal output
23	TE BPF	I	Input pin of tracking error through BPF
24	CROSS	O	Tracking error cross output
25	TE OUT	O	Tracking error signal output
26	TE-	I	Inverse input pin for tracking error amp
27	FE OUT	O	Output pin of focus error
28	FE-	I	Inverse input pin for focus error amp
29	FBAL	I	Focus balance control
30	TBAL	I	Tracking balance control
31	PDFR	I/O	F I-V amp gain control
32	PDER	I/O	E I-V amp gain control
33	PDF	I	I-V amp input
34	PDE	I	I-V amp input
35	PD BD	I	I-V amp input
36	PD AC	I	I-V amp input

■ BA15218N (IC32 / IC35) : Dual Ope. Amp.



■ BA3126N(IC31) : R/P Switch

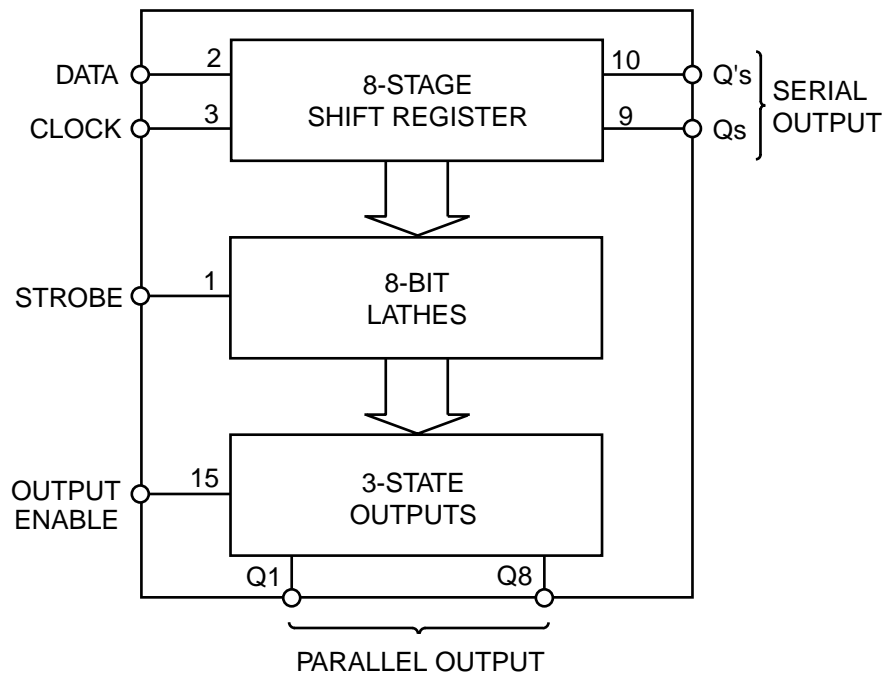


■ BU4094B(IC811):Serial to parallel port extension

1.Pin layout

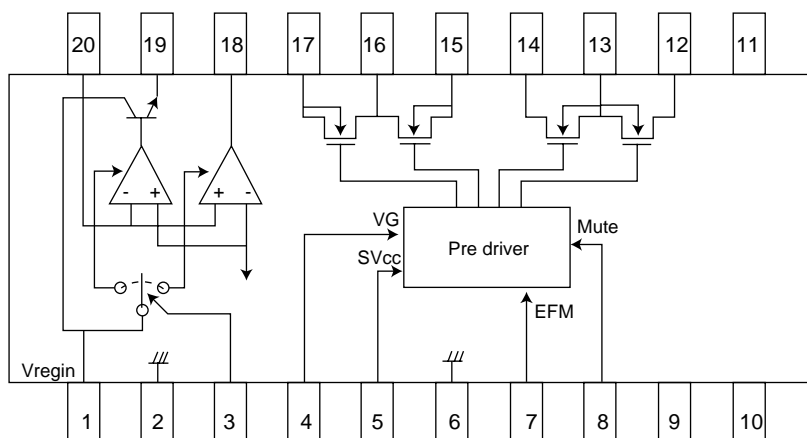
STROBE	1	16	Vdd
DATA	2	15	OUTPUT ENABLE
CLOCK	3	14	Q5
Q1	4	13	Q6
Q2	5	12	Q7
Q3	6	11	Q8
Q4	7	10	Q's
Vss	8	9	Qs

2.Block diagram



BD7910FV-X (IC450) : Pre driver

1. Block diagram

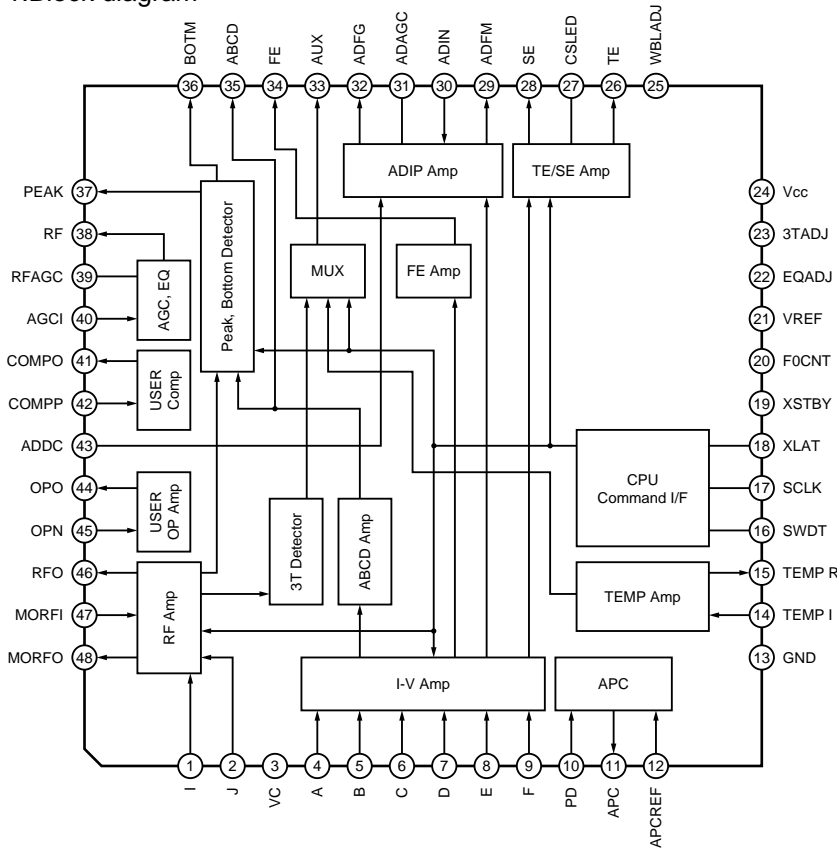


2. Pin function

Pin No.	Symbol	I/O	Function	Pin No.	Symbol	I/O	Function
1	Vreg IN	I	Regulator input and regulator power supply	11	NC	-	Non connect
				12	VOD2	○	Sync.output (Lower power MOS,drain)
2	Reg GN	-	Regulator GND	13	VSS	-	"H"bridge GND (Lower power MOS,source)
3	NC	-	Non connect	14	VOD1	○	Sync.output (Lower power MOS,drain)
4	VG	I	Voltage input for power MOS drive	15	VOS1	○	Source output (Upper power MOS,source)
5	SVCC	O	EFM high level output voltage	16	VDD	-	"H" bridge power supply terminal (Upper power MOS,source)
6	PDGND	-	Pre-driver GND				
7	EFM	I	EFM signal input	17	VOS2	○	Source output (Upper power MOS,source)
8	MUTE	I	Mute control (Low active)	18	Reg DRV	○	External PNP drive output for regulator
9	NC	O	Non connct	19	Reg OUT	○	Reglator output (Emitter follower output)
10	NC	O	Non connect	20	Reg NF	-	Regulator feedback terminal

■ CXA2523AR (IC310) : MD servo

1. Block diagram



2. Pin function

Pin No.	Symbol	I/O	Function
1	I	I	I-V converted RF signal I input.
2	J	I	I-V converted RF signal J input.
3	VC	O	Vcc/2 voltage output.
4	A	I	A current input for main beam servo signal.
5	B	I	B current input for main beam servo signal.
6	C	I	C current input for main beam servo signal.
7	D	I	D current input for main beam servo signal.
8	E	I	E current input for side beam servo signal.
9	F	I	F current input for side beam servo signal.
10	PD	I	Reflection light quantity monitor signal input.
11	APC	O	Laser APC output.
12	APCREF	I	Reference voltage input for the laser power intensity setting.
13	GND	-	Connect to GND.
14	TEMPI	I	Connects the temperature sensor.
15	TEMP R	I	Connects the temperature sensor. outputs the reference voltage.
16	SWDT	I	Data input for microcomputer serial interface.
17	SCLK	I	Shift clock input for microcomputer serial interface.
18	XLAT	I	Latch signal input for microcomputer serial interface. Latched when low.
19	XSTBY	I	Standby setting pin. Normal operation when high Standby when low.
20	F0CNT	I	Internal current source setting pin.

Pin No.	Symbol	I/O	Function
21	VREF	O	Reference voltage output.
22	EQADJ	I/O	Equalizer center frequency setting pin.
23	3TADJ	I/O	BPF3T center frequency setting pin.
24	Vcc	-	Power supply.
25	WBLADJ	I/O	BPF22 center frequency setting pin.
26	TE	O	Tracking error signal output.
27	CSLED	-	Connects the sled error signal LPF capacitor.
28	SE	O	Sled error signal output.
29	ADFM	O	ADIP FM signal output.
30	ADIN	I	ADIP signal comparator input.
31	ADAGC	-	Connects the ADIPAGC capacitor.
32	ADFG	O	ADIP2 binary value signal output.
33	AUX	O	13 output / temperature signal output. Switched with serial commands.
34	FE	O	Focus error signal output.
35	ABCD	O	Reflection light quantity signal output for the main beam servo detector.
36	BOTM	O	RF/ABCD bottom hold signal output.
37	PEAK	O	Peak hold signal output for the RF/ABCD signals.
38	RF	O	RF equalizer output.
39	RFAGC	-	Connects the RFAGC capacitor.
40	AGCI	I	RFAGC input.
41	COMPO	O	User comparator output.
42	COMPP	I	User comparator non-inverted input.
43	ADDC	I/O	Connects the capacitor for ADIP amplifier feedback circuit.
44	OPO	O	User operational amplifier output.
45	OPN	I	User operational amplifier inverted input.
46	RFO	O	RF amplifier output. Eye pattern checkpoint.
47	MORFI	I	Input of the groove RF signal with AC coupling.
48	MORFO	O	Groove RF signal output.

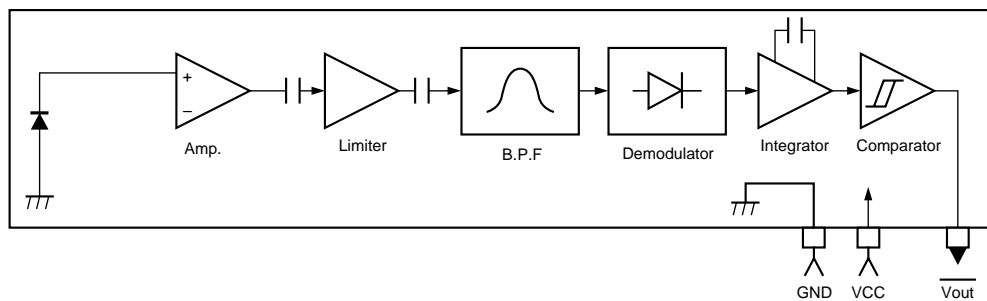
3.Pin function

Pin No.	Symbol	I/O	Function
1	MNT0	I/O	Monitor output.
2	MNT1	O	Monitor output.
3	MNT2	O	Monitor output.
4	MNT3	O	Monitor output.
5	SWDT	I	Data input for microcomputer serial interface.
6	SCLK	I	Shift clock input for microcomputer serial interface.
7	XLAT	I	Latch input for microcomputer serial interface.Latched at the falling edge.
8	SRDT	O	Data output for microcomputer serial interface.
9	SENS	O	Outputs the internal status corresponding to the microcomputer serial interface address.
10	XRST	I	Reset input. Low : reset
11	SQSY	O	Disc subcode Q sync / ADIP sync output.
12	DQSY	O	Subcode Q sync output in U-bit CD or MD format when the Digital In source is CD or MD.
13	RECP	I	Laser power switching input. High : recording power ; low ; playback power
14	XINT	O	Interruption request output. Low when the interruption status occurs.
15	TX	I	Enable signal input for recoding data output. High : enabled
16	OSCI	I	Crystal oscillation circuit input.
17	OSCO	O	Crystal oscillation circuit output. (inverted output ofthe OSCI pin)
18	XTSL	I	OSCI input frequency switching. XTSL1(command) = low and XTSL = high : 512Fs (22.5792MHz) XTSL1(command) = low and XTSL = low : 1024Fs (45.1584MHz) XTSL1(command) = high : 2048Fs (90.3168MHz)
19	DIN0	I	Digital audio interface signal input 1.
20	DIN1	I	Digital audio interface signal input 2.
21	DOUT	O	Digital audio interface signal output.
22	DATAI	I	Test pin. Connect to GND.
23	LRCKI	I	Test pin. Connect to GND.
24	XBCKI	I	Test pin. Connect to GND.
25	ADDT	I	Data input from A / D converter.
26	DADT	O	REC monitor output / decoded audio data output.
27	LRCK	O	LA clock (44.1kHz) output to the external audio block.
28	XBCK	O	Bit clock (2.8224kHz) output to the external audio block.
29	FS256	O	256Fs output.
30	DVDD	-	Digital power supply.
31	A03	O	External DRAM address output.
32	A02	O	External DRAM address output.
33	A01	O	External DRAM address output.
34	A00	O	External DRAM address output.
35	A10	O	External DRAM address output.
36	A04	O	External DRAM address output.
37	A05	O	External DRAM address output.
38	A06	O	External DRAM address output.
39	A07	O	External DRAM address output.
40	A08	O	External DRAM address output.
41	A11	O	External DRAM address output.
42	DVSS	-	Digital ground.
43	XOE	O	External DRAM output enable.

Pin No.	Symbol	I/O	Function
44	XCAS	O	External DRAM CAS output.
45	A09	O	External DRAM address output.
46	XRAS	O	External DRAM RAS output.
47	XWE	O	External DRAM write enable.
48	D1	I/O	External DRAM data bus.
49	D0	I/O	External DRAM data bus.
50	D2	I/O	External DRAM data bus.
51	D3	I/O	External DRAM data bus.
52	MDDTI	I	MD-DATA mode 1 switching input. (Low : normal mode ; high : MD-DATA mode 1)
53	ASYO	O	Playback EFM full-swing output. (Low : vss ; jigh : Vdd)
54	ASYI	I	Playback EFM comparator slice voltage input.
55	AVDD	-	Analog power supply.
56	BIAS	I	Playback EFM comparator bias current input.
57	RFI	I	Playback EFM RE signal input.
58	AVSS	-	Analog ground.
59	PCO	O	Phase comparison output for master PLL of playback digital PLL and recording EFM PLL.
60	FILI	I	Filter input for master PLL of playback digital PLL and recording EFM PLL.
61	FILO	O	Filter output for master PLL of playback digital PLL and recording EFM PLL.
62	CLTV	I	Internal VCO control voltage input for master PLL of playback digital EFM PLL and recording EFM PLL.
63	PEAK	I	Peak hold signal input for quantity of light.
64	BOTM	I	Bottom hold signal input for quantity of light.
65	ABCD	I	Signal input for quantity of light.
66	FE	I	Focus error signal input.
67	AUXI	I	Auxillary input 1.
68	VC	I	Center voltage input.
69	ADIO	I	Monitor output for A / D converter input signal.
70	AVDD	-	Analog power supply.
71	ADRT	I	Voltage input for the upper limit of the A / D converter operating range.
72	ADRB	I	Voltage input for the lower limit of the A / D converter operating range.
73	AVSS	-	Analog ground.
74	SE	I	Sled error signal input.
75	TE	I	Tracking error signal input.
76	DCHG	I	Connect to the low-impedance power supply.
77	APC	I	Error signal input for laser digital APC.
78	ADFG	I	ADIP binary FM signal (22.05 ± 1kHz) input.
79	F0CNT	O	CXA2523 current source setting output.
80	XLRF	O	CXA2523 control latch output. Latched at the falling edge.
81	CKRF	O	CXA2523 control shift clock output.
82	DTRF	O	CXA2523 control data output.
83	APCREF	O	Reference PWM output for laser APC.
84	LDDR	O	PWM output for laser digital APC.
85	TRDR	O	Tracking servo drive PWM output. (-)
86	TFDR	O	Tracking servo drive PWM output. (+)
87	DVDD	-	Digital power supply.
88	FFDR	O	Focus servo drive PWM output. (+)
89	FRDR	O	Focus servo drive PWM output. (-)
90	FS4	O	4Fs output. (176.4kHz)

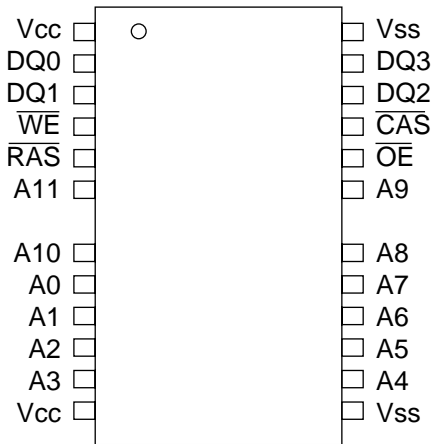
Pin No.	Symbol	I/O	Function
91	SRDR	O	Sled servo drive PWM output. (-)
92	SFDR	O	Sled servo drive PWM output. (+)
93	SPRD	O	Spindle servo drive output. (PWM (-) or polarity)
94	SPFD	O	Spindle servo drive output. (PWM (+) or PWM absolute value)
95	FGIN	I	Spindle CAV servo FG input.
96	TEST1	I	Test pin. Connect to GND.
97	TEST2	I	Test pin. Connect to GND.
98	TEST3	I	Test pin. Connect to GND.
99	DVSS	-	Digital ground.
100	EFMO	O	Low when playback ; EFM (encoded data) output when recording.

■ GP1U271X (IC701) : Receiver for remote



■HY51V17400CT-60 (IC390) : DRAM

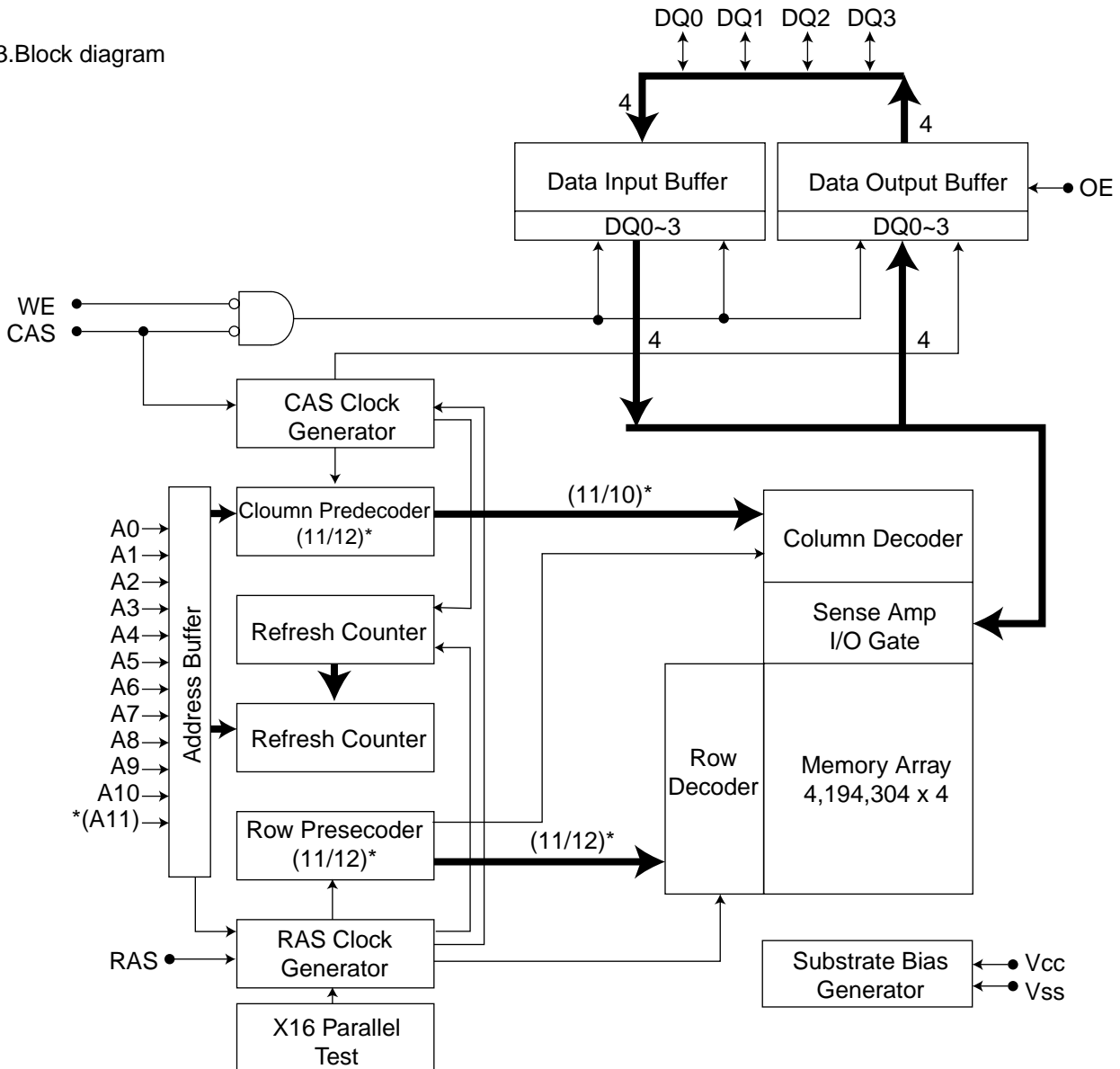
1.Pin layout



2.Pin function

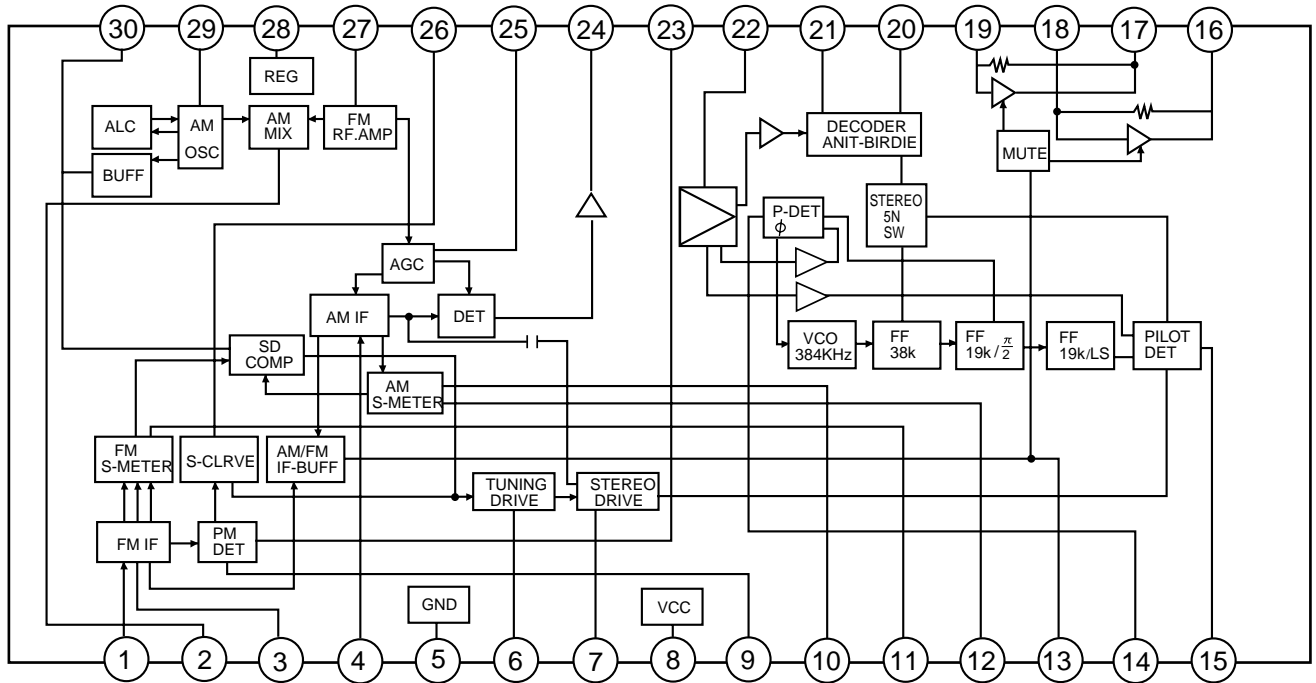
Pin Name	Parameter
/RAS	Row Address Strobe
/CAS	Column Address Strobe
/WE	Write Enable
/OE	Output Enable
A0~A11	Address Input (4K Refresh Product)
A0~A10	Address Input (2K Refresh Product)
DQ0~DQ3	Data In/Out
Vcc	Power (3.3V)
Vss	Ground
NC	No Connection

3.Block diagram



■ LA1838(IC1): FM AM IF AMP&detector, FM MPX decoder

1. Block Diagram

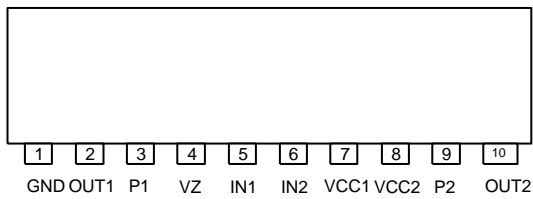


2. Pin Function

Pin No.	Symbol	I/O	Function	Pin No.	Symbol	I/O	Function
1	FM IN	I	This is an input terminal of FM IF signal.	16	L OUT	O	Left channel signal output.
2	AM MIX	O	This is an out put terminal for AM mixer.	17	R OUT	O	Right channel signal output.
3	FM IF	I	Bypass of FM IF	18	L IN	I	Input terminal of the left channel post AMP.
4	AM IF	I	Input of AM IF Signal.	19	R IN	I	Input terminal of the right channel post AMP.
5	GND	—	This is the device ground terminal.	20	RO	O	Mpx Right channel signal output.
6	TUNED	O	When the set is tuning, this terminal becomes "L".	21	LO	O	Mpx Left channel signal output.
7	STEREO	O	Stereo indicator output. Stereo "L", Mono: "H"	22	IF IN	I	Mpx input terminal
8	VCC	—	This is the power supply terminal.	23	FM OUT	O	FM detection output.
9	FM DET	—	FM detect transformer.	24	AM DET	O	AM detection output.
10	AM SD	—	This is a terminal of AM ceramic filter.	25	AM AGC	I	This is an AGC voltage input terminal for AM
11	FM VSM	O	Adjust FM SD sensitivity.	26	AFC	—	This is an output terminal of voltage for FM-AFC.
12	AM VSM	O	Adjust AM SD sensitivity.	27	AM RF	I	AM RF signal input.
13	MUTE	I/O	When the signal of IF REQ of IC121(LC72131) appear, the signal of FM/AM IF output. //Muting control input.	28	REG	O	Register value between pin 26 and pin28 desides the frequency width of the input signal.
14	FM/AM	I	Change over the FM/AM input. "H" :FM, "L" : AM	29	AM OSC	—	This is a terminal of AM Local oscillation circuit.
15	MONO/ST	O	Stereo : "H", Mono: "L"	30	OSC BUFFER	O	AM Local oscillation Signal output.

■ **LB1641 (IC851,IC852) : DC motor driver**

1. Pin layout



2. Pin function

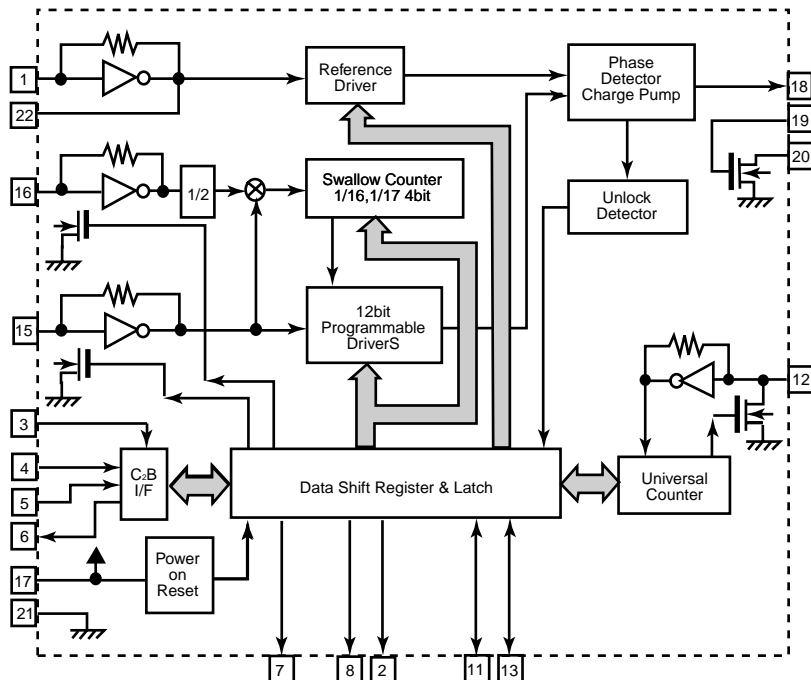
Input		Output		Mode
IN1	IN2	OUT1	OUT2	
0	0	0	0	Brake
1	0	1	0	CLOCKWISE
0	1	0	1	COUNTER-CLOCKWISE
1	1	0	0	Brake

■ LC72136N (IC2) : PLL frequency synthesizer

1. Pin layout

XT	1	22	XT
FM/AM	2	21	GND
CE	3	20	LPFOUT
DI	4	19	LPFIN
CLOCK	5	18	PD
DO	6	17	VCC
FM/ST/VCO	7	16	FMIN
AM/FM	8	15	AMIN
	9	14	
	10	13	IFCONT
SDIN	11	12	IFIN

2. Block diagram

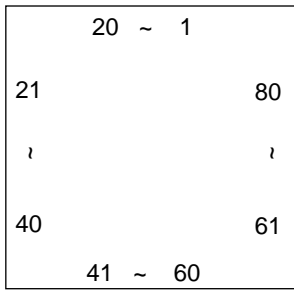


3. Pin function

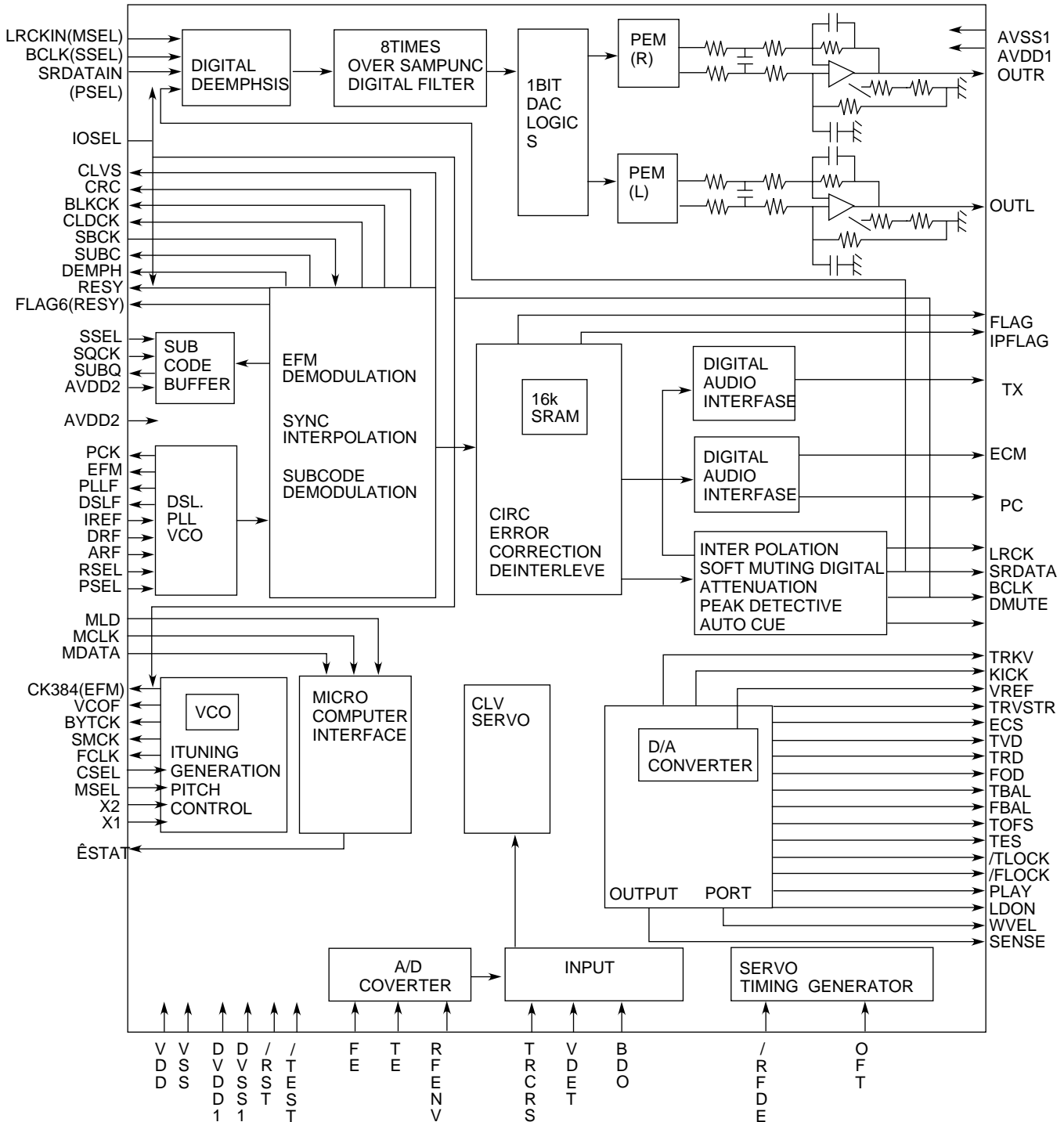
Pin No.	Symbol	I/O	Function	Pin No.	Symbol	I/O	Function
1	XT	I	X'tal oscillator connect (75kHz)	12	IFIN	I	IF counter signal input
2	FM/AM	O	LOW:FM mode	13	IFCONT	O	IF signal output
3	CE	I	When data output/input for 4pin(input) and 6pin(output): H	14		-	Not use
4	DI	I	Input for receive the serial data from controller	15	AMIN	I	AM Local OSC signal output
5	CLOCK	I	Sync signal input use	16	FMIN	I	FM Local OSC signal input
6	DO	O	Data output for Controller Output port	17	VCC	-	Power suply(VDD=4.5-5.5V) When power ON:Reset circuit move
7	FM/ST/VCO	O	"Low": MW mode	18	PD	O	PLL charge pump output(H: Local OSC frequency Height than Reference frequency. L: Low Agreement: Height impedance)
8	AM/FM	O	Open state after the power on reset	19	LPFIN	I	Input for active lowpassfilter of PLL
9	LW	I/O	Input/output port	20	LPFOUT	O	Output for active lowpassfilter of PLL
10	MW	I/O	Input/output port	21	GND	-	Connected to GND
11	SDIN	I/O	Data input/output	22	XT	I	X'tal oscillator(75KHz)

■ MN35510(IC501):Digital servo & Digital signal processor

1. Terminal Layout



2. Block Diagram

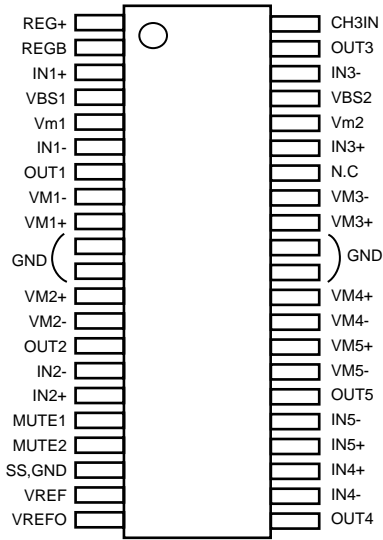


3. Description

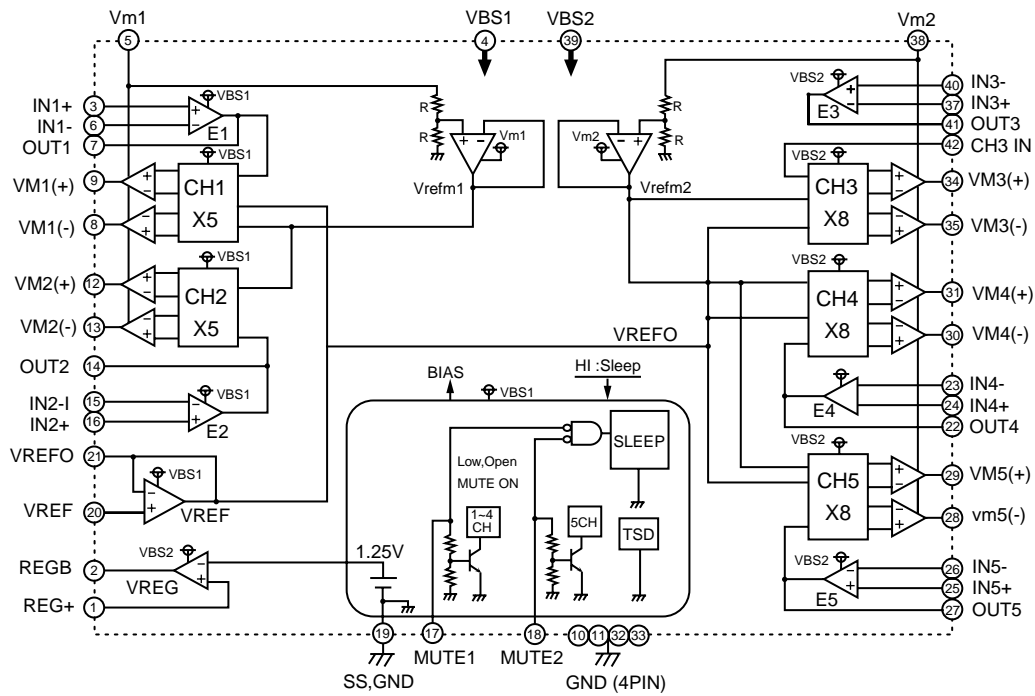
Pin No.	symbol	I/O	Description	Pin No.	symbol	I/O	Description
1	BCLK	O	Not used	41	TES	O	Tracking error shunt signal output(H:shunt)
2	LRCK	O	Not used	42	PLAY	—	Not used
3	SRDATA	O	Not used	43	WVEL	—	Not used
4	DVDD1	—	Power supply (Digital)	44	ARF	I	RF signal input
5	DVSS1	—	Connected to GND	45	IREF	I	Reference current input pin
6	TX	O	Digital audio interface output	46	DRF	I	Bias pin for DSL
7	MCLK	I	Micom command clock signal input (Data is latched at signal's rising point)	47	DSLFL	I/O	Loop filter pin for DSL
8	MDATA	I	Micom command data input	48	PLLFL	I/O	Loop filter pin for PLL
9	MLD	I	Micom command load signal input	49	VCOF	—	Not used
10	SENSE	O	Sence signal output	50	AVDD2	—	Power supply(Analog)
11	FLOCK	O	Focus lock signal output Active :Low	51	AVSS2	—	Connected to GND(Analog)
12	TLOCK	O	Tracking lock signal output Active :Low	52	EFM	—	Not used
13	BLKCK	O	sub-code - block - clock signal output	53	PCK	—	Not used
14	SQCK	I	Outside clock for sub-code Q resister input	54	PDO	—	Not used
15	SUBQ	O	Sub-code Q -code output	55	SUBC	—	Not used
16	DMUTE	—	Connected to GND	56	SBCK	—	Not used
17	STATUS	O	Status signal (CRC,CUE,CLVS,TTSTOP,ECLV,SQOK)	57	VSS	—	Connected to GND(for X'tal oscillation circuit)
18	RST	I	Reset signal input (L:Reset)	58	XI	I	Input of 16.9344MHz X'tal oscillation circuit
19	SMCK	—	Not used	59	X2	O	Output of X'tal oscillation circuit
20	PMCK	—	Not used	60	VDD	—	Power supply(for X'tal cscillation circuit)
21	TRV	O	Traverse enforced output	61	BYTCK	—	Not used
22	TVD	O	Traverse drive output	62	CLDCK	—	Not used
23	PC	—	Not used	63	FLAG	—	Not used
24	ECM	O	Spindle motor drive signal (Enforced mode output) 3-State	64	IPPLAG	—	Not used
25	ECS	O	Spindle motor drive signal (Servo error signal output)	65	FLAG	—	Not used
26	KICK	O	Kick pulse output	66	CLVS	—	Not used
27	TRD	O	Tracking drive output	67	CRC	—	Not used
28	FOD	O	Focus drive output	68	DEMPH		Not used
29	VREF	I	Reference voltage input pin for D/A output block (TVD,FOD,FBA,TBAL)	69	RESY	—	Not used
30	FBAL	O	Focus Balance adjust signal output	70	IOSEL	—	pull up
31	TBAL	O	Tracking Balance adjust signal output	71	TEST	—	pull up
32	FE	I	Focus error signal input(Analog input)	72	AVDD1	—	Power supply(Digital)
33	TE	I	Tracking error signal input(Analog input)	73	OUT L	O	Lch audio output
34	RF ENV	I	RF envelope signal input(Analog input)	74	AVSS1	—	Connected to GND
35	VDET	I	Vibration detect signal input(H:detect)	75	OUT R	O	Rch audio output
36	OFT	I	Off track signal input(H:off track)	76	RSEL	—	pull up
37	TRCRS	I	Track cross signal input	77	CSEL	—	Connected to GND
38	RFDET	I	RF detect signal input(L:detect)	78	PSEL	—	Connected to GND
39	BDO	I	BDO input pin(L:detect)	79	MSEL	—	Connected to GND
40	LDON	O	Laser ON signal output(H:on)	80	SSEL	—	Pull up

■ M63008FP-X (IC410) : 5ch Actuator driver

1.Pin layout

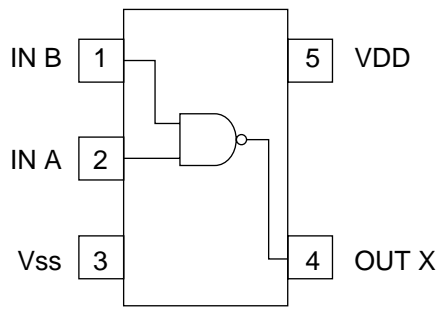


2.Block diagram

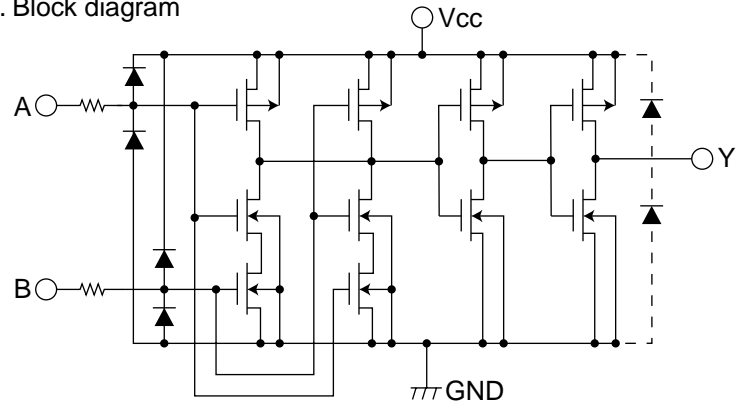


■ TC7S08F-W (IC340) : Buffer

1. Pin layout

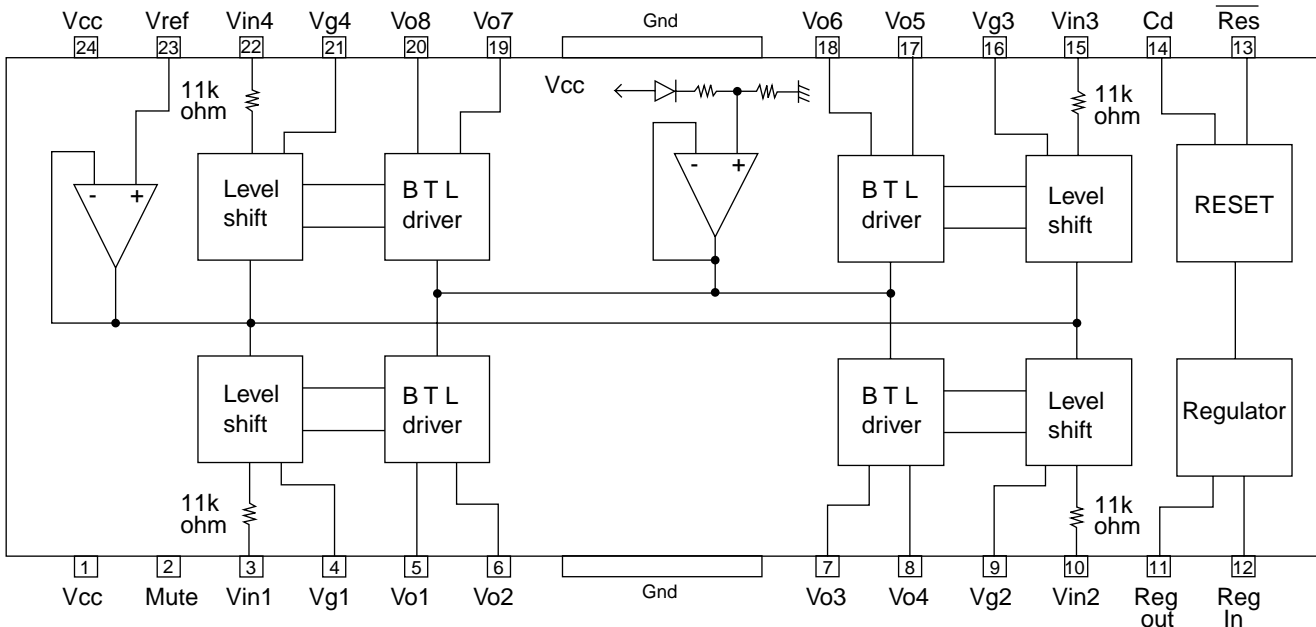


2. Block diagram



■ LA6541-X (IC801) : Servo driver

1. Pin Layout & block diagram



2. Pin function

Pin No.	Symbol	Function
1	Vcc	Power supply (Shorted to pin 24)
2	Mute	All BTL amplifier outputs ON/OFF
3	Vin1	BTL AMP 1 input pin
4	Vg1	BTL AMP 1 input pin (For gain adjustment)
5	Vo1	BTL AMP 1 input pin (Non inverting side)
6	Vo2	BTL AMP 1 input pin (Inverting side)
7	Vo3	BTL AMP 2 input pin (Inverting side)
8	Vo4	BTL AMP 2 input pin (Non inverting side)
9	Vg2	BTL AMP 2 input pin (For gain adjustment)
10	Vin2	BTL AMP 2 input pin
11	Reg Out	External transistor collector (PNP) connection. 5V power supply output
12	Reg In	External transistor (PNP) base connection
13	Res	Reset output
14	Cd	Reset output delay time setting (Capacitor connected externally)
15	Vin3	BTL AMP 3 input pin
16	Vg3	BTL AMP 3 input pin (For gain adjustment)
17	Vo5	BTL AMP 3 output pin (Non inverting side)
18	Vo6	BTL AMP 3 output pin (Inverting side)
19	Vo7	BTL AMP 4 output pin (Inverting side)
20	Vo8	BTL AMP 4 output pin (Non inverting side)
21	Vg4	BTL AMP 4 output pin (For gain adjustment)
22	Vin4	BTL AMP 4 output pin
23	Vref	Level shift circuit's reference voltage application
24	Vcc	Power supply (Shorted to pin 1)

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